Implementing Systems with Two Point Boundary Conditions for A CACSD Package of Sampled-Data Systems

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Abstract—A library for systems with two point boundary conditions is implemented on MATLAB for development of a CACSD package for sampled-data systems. It is shown that we can handle various setups of sampled-data systems including multirate control and input/output time-delay in a unified way by using the systems with two point boundary conditions. Based on the developed library with object-oriented programming feature, it is easy to implement analysis and design algorithms of sampled-data systems with high readability.

I. INTRODUCTION

Theory for analysis and design of sampled-data feedback control systems has been successfully developed in the last decade. Now we can take into account of intersample behavior of sampled-data systems in analysis and synthesis. See [2] and references therein. In spite of the development of the theory, however, there are less real applications. One of the main reasons would be that we do not have a widely spread CACSD package for sampled-data systems.

This paper concerns with a CACSD package for sampleddata systems. Actually we implement a library for systems with two point boundary conditions [8] on MATLAB¹ for easy development of a CACSD package.

This work is motivated as follows: Consider a sampleddata system depicted in Fig. 1, where G_c , K_d , S, and H are a continuous-time and a discrete-time systems, a sampler, and a hold respectively. It has been shown that there is a discretetime system G_s such that the following are equivalent:

- (i) L₂-induced norm from w_c to z_c is less than 1
- (ii) \mathbf{H}_{∞} norm of a feedback connection of G_s and K_d is less than 1

under some reasonable assumptions [1], [6], [7]. Following [1], for example, G_s is given by



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Fig. 1. Sampled-Data Feedback Control Systems

where matrices and operators in the RHS of (1) are from the state-space realization of the lifted system [1], [9] of *G* in Fig. 1 (See details below):

$$\begin{bmatrix} x[k+1] \\ z[k] \\ y[k] \end{bmatrix} = \begin{bmatrix} A & \dot{B}_1 & B_2 \\ \dot{C}_1 & \breve{D}_{11} & \dot{D}_{12} \\ C_2 & \dot{D}_{21} & D_{22} \end{bmatrix} \begin{bmatrix} x[k] \\ w[k] \\ u[k] \end{bmatrix}$$
(2)

In a CACSD package for sampled-data systems, we should implement (1) in a numerically computable form. Actually we can reduce the computation in (1) to that of a matrix exponential:

$$\begin{bmatrix} B_{s1}B'_{s1} & [A_s & B_{s2}] \\ \begin{bmatrix} A'_s \\ B'_{s2} \end{bmatrix} & \begin{bmatrix} C'_{s1} \\ D'_{s12} \end{bmatrix} \begin{bmatrix} C'_{s1} \\ D'_{s12} \end{bmatrix}'$$

$$= \begin{bmatrix} \frac{\Gamma_{11}}{\Gamma_{12}} & \frac{\Gamma_{13}}{\Gamma_{41}} \\ \Gamma_{41} & \Gamma_{42} & \Gamma_{43} \end{bmatrix} \begin{bmatrix} 0 & I & 0 \\ 0 & I \\ -\Gamma_{33}^{-1} & -\Gamma_{33}^{-1}\Gamma_{31} & -\Gamma_{33}^{-1}\Gamma_{32} \end{bmatrix}$$

$$(3)$$

$$\begin{bmatrix} A_u & B_u \\ C_u & * \end{bmatrix} := \begin{bmatrix} A_c & B_{c2} & B_{c1} \\ 0 & 0 & 0 \\ \hline C_{c1} & D_{c12} & * \end{bmatrix}$$

if S is an ideal sampler of period h > 0, H is a ZOH, and G_c is given by

$$G_{c}:\begin{bmatrix}\dot{x}_{c}(t)\\z_{c}(t)\\y_{c}(t)\end{bmatrix} = \begin{bmatrix}A_{c} & B_{c1} & B_{c2}\\C_{c1} & D_{c11} & D_{c12}\\C_{c2} & D_{c21} & 0\end{bmatrix}\begin{bmatrix}x_{c}(t)\\w_{c}(t)\\u_{c}(t)\end{bmatrix}.$$
 (4)

with $D_{c11} = 0$ and $D_{c21} = 0$. Hence we can implement (4) instead of (1). Implementations are found in [3], [5].

We, however, encounter cases of S is not an ideal sampler or H is not a ZOH in practical setups of sampled-data systems as we will see below. In the cases we can not use the matrix exponential formula (4) although the operator formula (1) is still true since matrices and operators in (2) are different (See details below).

In order to avoid to derive matrix exponential formulas for the cases individually, it is desirable to develop a software environment such that we can implement the operator formula (1) directly. It also enhances the readability of the program and makes development and maintenance easy. We can develop such software environment as a library on the object-oriented programming language. In this paper, we use the systems with two point boundary conditions [8] for unified representation of operators in (2) and MathScript on MATLAB as an object-oriented programming language.

This paper is organized as follows: Section II reviews the theory for systems with two point boundary conditions and derives a condition to check minimality of the systems. Section III shows that we can unify representation of operators related to lifted sampled-data systems in practical setups by using the systems with two point boundary conditions. Section IV describes the detail of the implementation. Section V concludes this paper.

II. SYSTEMS WITH TWO POINT BOUNDARY CONDITIONS

Mirkin and Palmor [8] proposed to use the state-space systems with two point boundary conditions, SSBC for short, as a unified representation of the operators related to the lifting [1], [9] of sampled-data systems, together with the impulse and the sample operators.

In this section, we review basic properties of SSBCs taken from [8]. We also derive conditions to check the minimality of SSBCs, those are easily implementable on a CACSD package.

Definition 1: We call the following linear differential equation with a boundary constraint as an SSBC:

$$\begin{bmatrix} \dot{x}(t) \\ y(t) \end{bmatrix} = \begin{bmatrix} A & B \\ C & D \end{bmatrix} \begin{bmatrix} x(t) \\ u(t) \end{bmatrix}, \quad \Omega x(0) + \Upsilon x(h) = 0 \quad (5)$$

where h > 0. We also define the impulse and the sample operators by

$$(\mathscr{I}_{\theta}v)(t) := v\delta(t-\theta), \quad \mathscr{S}_{\theta}w := w(\theta) \tag{6}$$

where $\theta \in [0, h]$ and δ denotes the Dirac's delta function. Properties 1 and 2 below are found in [8]:

Property 1: G in (5) is well-posed if and only if

$$\Xi := \Omega + \Upsilon e^{Ah} \tag{7}$$

is nonsingular.

Property 2: Let G in (5) and H are well-posed SSBCs. 1) G+H, GH, G^* are all well-posed SSBCs.

2) G^{-1} is a well-posed SSBC.

 \Leftrightarrow Both *D* and $\Omega + \Upsilon e^{(A-BD^{-1}C)h}$ are nonsingular.

- 3) $(\mathscr{S}_{\theta}G)^* = G^*\mathscr{I}_{\theta}, \ (G\mathscr{I}_{\theta})^* = \mathscr{S}_{\theta}G^*.$
- 4) Let $h_1, h_2 \in [0, h]$ and D = 0, then

$$\mathscr{S}_{h_1} G \mathscr{I}_{h_2} = \begin{cases} C e^{Ah_1} \Xi^{-1} \Omega e^{-Ah_2} B & (h_1 > h_2) \\ -C e^{Ah_1} \Xi^{-1} \Upsilon e^{A(h-h_2)} B & (h_1 < h_2) \end{cases}$$

In the sequel, we use the following compact notation to represent an SSBC G in (5):

$$G = \left(\left[\begin{array}{cc} A & B \\ C & D \end{array} \right], \Omega, \Upsilon \right).$$

In order to reduce the size of the matrix exponential in Property 2 4), which is required when we perform an "equivalent" discretization of sampled-data systems, the following conditions are derived:

- Property 3: The following statements are equivalent:
- (i) (5) is a minimal realization.
- (ii) The following state-space realization is minimal:

$$\begin{bmatrix} \dot{x}(t) \\ y(t) \end{bmatrix} = \begin{bmatrix} A & \Xi^{-1}\Omega M_{\rm C} & \Xi^{-1}\Upsilon M_{\rm C} \\ \hline M_{\rm O}\Xi^{-1}\Omega & * & * \\ M_{\rm O}\Xi^{-1}\Upsilon & * & * \end{bmatrix} \begin{bmatrix} x(t) \\ u(t) \end{bmatrix}$$
(8)

where

$$M_{\mathbf{C}} := \begin{bmatrix} B & AB & \cdots & A^{n-1}B \end{bmatrix},$$
$$M_{\mathbf{O}} := \begin{bmatrix} C' & A'C' & \cdots & (A')^{n-1}C' \end{bmatrix}'$$

Moreover, let A_r be the 'A'-term of a minimal realization of (8) and defined by the related coordinate transformation T:

$$T^{-1}AT =: \left[\begin{array}{cc} A_r & * \\ * & * \end{array} \right].$$

Then a minimal realization of (5) is given by

$$\left(\left[\begin{array}{cc}A_r & B_r\\ C_r & D\end{array}\right], \Omega_r, \Upsilon_r\right)$$

where

$$T^{-1}B \coloneqq \begin{bmatrix} B_r \\ * \end{bmatrix}, \quad CT \coloneqq \begin{bmatrix} C_r & * \end{bmatrix},$$
$$T^{-1}\Xi^{-1}\Omega T \coloneqq \begin{bmatrix} \Omega_r & * \\ * & * \end{bmatrix}, \quad T^{-1}\Xi^{-1}\Upsilon T \coloneqq \begin{bmatrix} \Upsilon_r & * \\ * & * \end{bmatrix}.$$

The proof is found in appendix.

III. SAMPLED-DATA SYSTEMS

In order to develop a CACSD package, one of the important steps is to determine the class of systems which should be handled. In this paper we consider sampled-data systems such that the operators in the state space representation (2) of the related lifted system are given by SSBCs and linear combinations of sample and impulse operators:

$$\tilde{\mathscr{I}} = \sum_{i=1}^{n_S} \mathscr{S}_{\eta_{Si}} M_{Si}, \quad \tilde{\mathscr{I}} = \sum_{i=1}^{n_J} M_{Ji} \mathscr{I}_{\eta_{Ji}}$$
(9)

where η_{S_i} , $\eta_{J_i} \in [0, h)$ and M_{S_i} 's and M_{J_i} 's are coefficient matrices.

Remark 1: A sum of SSBCs is again an SSBC (Property 2 1)). The same property holds for $\tilde{\mathscr{I}}$, $\tilde{\mathscr{I}}$, product of $\tilde{\mathscr{I}}$ and an SSBC, and that of an SSBC and $\tilde{\mathscr{I}}$. For example, let P_1 and P_2 are SSBCs, then we have

$$\tilde{\mathscr{I}}_1 P_1 + \tilde{\mathscr{I}}_2 P_2 = \begin{bmatrix} \tilde{\mathscr{I}}_1 & \tilde{\mathscr{I}}_2 \end{bmatrix} \begin{bmatrix} P_1 \\ P_2 \end{bmatrix}$$

In this section, we point out that a wide variety of sampleddata systems including multirate control and/or time-delay setups satisfy our assumption.

Consider first a standard sampled-data feedback system depicted in Fig. 1, where G_c is a continuous-time generalized plant given by (4). K_d is a discrete-time system. The sampler *S* mapping continuous-time signal y_c to a discrete-time signal *y* and the hold *H* mapping a discrete-time signal *u* to a continuous-time signal u_c are supposed to have the form:

$$S: y[k] = \tilde{\mathscr{I}}_S P_S(\mathscr{L} y_c)[k-1] + E_S y_c(kh), \qquad (10)$$

$$H: (\mathscr{L}u_c)[k](\theta) = (P_H \mathscr{I}_H)(\theta)u[k], \quad \theta \in [0, h)$$
(11)

respectively, where E_S is a matrix of compatible size. $\tilde{\mathscr{I}}_S$ and $\tilde{\mathscr{I}}_H$ are linear combinations of sample and impulse operators as in (9). P_S and P_H are SSBCs defined by

$$P_{S} := \left(\left[\begin{array}{cc} A_{S} & B_{S} \\ C_{S} & D_{S} \end{array} \right], I, 0 \right), \quad P_{H} := \left(\left[\begin{array}{cc} A_{H} & B_{H} \\ C_{H} & 0 \end{array} \right], I, 0 \right).$$

The symbol \mathscr{L} denotes the lifting [1], [9] mapping a continuous-time signal f_c to a discrete-time signal f_d taking values in a function space:

$$\mathscr{L}: f_c \mapsto f_d, \quad f_d[k](\boldsymbol{\theta}) := f_c(kh + \boldsymbol{\theta}), \quad \boldsymbol{\theta} \in [0, h).$$

Then the lifted system of G in Fig. 1, namely,

$$\begin{bmatrix} \mathscr{L} & 0 \\ 0 & S \end{bmatrix} G_c \begin{bmatrix} \mathscr{L}^{-1} & 0 \\ 0 & H \end{bmatrix}$$

has a state-space representation (2) by defining $z := \mathscr{L} z_c$ and $w := \mathscr{L} w_c$, where matrices and operators are defined in (12) if $n_S = 0$, and in (13) if $n_S \ge 1$, respectively, where F_S is a column full rank matrix obtained by eliminating *i*-th column of the identity for all *i*'s such that *i*-th standard basis satisfies $e'_i \mathscr{\tilde{S}}_S = 0$. We also use the following definition:

$$\begin{bmatrix} \breve{A} & \breve{B}_1 & \breve{B}_2 \\ \breve{C}_1 & \breve{D}_{11} & \breve{D}_{12} \\ \breve{C}_2 & \breve{D}_{21} & \breve{D}_{22} \end{bmatrix} := \left(\begin{bmatrix} A_c & I & B_{c1} & B_{c2} \\ \hline I & 0 & 0 & 0 \\ C_{c1} & 0 & D_{c11} & D_{c12} \\ C_{c2} & 0 & D_{c21} & 0 \end{bmatrix}, I, 0 \right).$$

Remark 2: We need to assume

(i) $E_S D_{c21} = 0$.

(ii) $M_{Si}D_SD_{c21} = 0$ for $i = 1, 2, ..., n_S$.

for the L_2 -stability of the system.

In the rest of this section, we show three practical setups of sampled-data systems those we can handle.

1) Multirate Control: Let us consider the situation where the sampling period is large because of the slow sensor or other constraints, while the processor to implement the discrete-time controller has sufficient computational power. We can find such situation in, e.g., hard disk drive control. Under this condition, it would be useful to switch control input at some points in the intersample, as well as at sampling instants, in order to enhance the performance of the system.

To be more concrete, we consider the following multirate control to determine the continuous-time control input u_c from the discrete-time measured output y:

$$u_{c}(kh+\theta) = \begin{cases} u_{1}[k], & \theta \in [0,h_{1}) \\ u_{2}[k], & \theta \in [h_{1},h) \end{cases},$$
$$\begin{bmatrix} x_{K}[k+1] \\ u_{1}[k] \\ u_{2}[k] \end{bmatrix} = \begin{bmatrix} A_{K} & B_{K} \\ C_{K1} & D_{K1} \\ C_{K2} & D_{K2} \end{bmatrix} \begin{bmatrix} x_{K}[k+1] \\ y[k] \end{bmatrix},$$

while G_c and S are the same in the standard setup.

Although the discrete-time dynamics above is periodic, we can cast this multirate control setup into the standard setup by setting

$$\begin{bmatrix} A & \acute{B}_{1} & B_{2} \\ \acute{C}_{1} & \breve{D}_{11} & \acute{D}_{12} \\ C_{2} & \acute{D}_{21} & D_{22} \end{bmatrix} := \begin{bmatrix} \mathscr{S}_{h} & 0 & 0 \\ 0 & I & 0 \\ 0 & 0 & \mathscr{S}_{0}E_{S} \end{bmatrix} \begin{bmatrix} \check{A} & \check{B}_{1} & \check{B}_{2} \\ \check{C}_{2} & \check{D}_{21} & \check{D}_{22} \end{bmatrix} \begin{bmatrix} \mathscr{I}_{0} & 0 & 0 \\ 0 & I & 0 \\ 0 & 0 & P_{H}\tilde{\mathscr{I}}_{H} \end{bmatrix}$$
(12)
$$\begin{bmatrix} A & \acute{B}_{1} & B_{2} \\ \acute{C}_{1} & \breve{D}_{11} & \acute{D}_{12} \\ C_{2} & \acute{D}_{21} & D_{22} \end{bmatrix} := \begin{bmatrix} \mathscr{S}_{h} & 0 & 0 & 0 \\ 0 & \mathscr{I} & 0 \\ 0 & 0 & I & 0 \\ \hline{0} & 0 & I & 0 \\ \hline{0} & 0 & 0 & I \end{bmatrix} \begin{bmatrix} \check{A} & 0 & \check{B}_{1} & \check{B}_{2} \\ \check{C}_{2} & 0 & \check{D}_{21} & \check{D}_{22} \\ \check{C}_{1} & 0 & \check{D}_{11} & \check{D}_{12} \\ \mathscr{S}_{0}E_{S}\check{C}_{2} & F_{S} & \mathscr{S}_{0}E_{S}\check{D}_{21} & \mathscr{S}_{0}E_{S}\check{D}_{22} \end{bmatrix} \begin{bmatrix} \mathscr{I}_{0} & 0 & 0 & 0 \\ 0 & I & 0 & 0 \\ 0 & 0 & I & 0 \\ 0 & 0 & 0 & I & 0 \\ \end{bmatrix}$$
(13)



Fig. 2. Sampled-Data Systems with Input Time-Delays

$$K_d: \begin{bmatrix} x_K[k+1] \\ u[k] \end{bmatrix} = \begin{bmatrix} A_K & B_K \\ \hline C_{K1} & D_{K1} \\ C_{K2} & D_{K2} \end{bmatrix} \begin{bmatrix} x_K[k+1] \\ y[k] \end{bmatrix}$$

and define H by

$$P_{H} = \left(\begin{bmatrix} 0 & 0 & | I & 0 \\ 0 & 0 & 0 & I \\ \hline I & I & 0 & 0 \end{bmatrix}, I, 0 \right),$$

and

$$ilde{\mathscr{I}}_{H} = \left[egin{array}{cc} \mathscr{I}_{0} - \mathscr{I}_{h_{1}} & 0 \\ 0 & \mathscr{I}_{h_{1}} \end{array}
ight].$$

2) Input Delays: Consider a sampled-data system with time-delays Σ at control input as depicted in Fig. 2. The following property is essentially the same that in [4]:

Property 4: Given a hold $H: u \mapsto v_c$:

$$(\mathscr{L}u_c)[k](\theta) = (P_H\mathscr{I}_{\eta})(\theta)u[k], \quad \theta \in [0,h)$$

and a time-delay Σ : $v_c \mapsto u_c$:

$$u_c(t) = v_c(t - \sigma)$$

where η , $\sigma \in [0, h)$. Then $u_c = \Sigma H u$ satisfies

$$(\mathscr{L}u_c)[k](\theta) = \left(P_{H1}\tilde{\mathscr{I}}_{H1}\right)(\theta) \left[\begin{array}{c} u[k-1] \\ u[k] \end{array} \right]$$

if $\eta + \sigma \leq h$, where

$$P_{H1} := \begin{bmatrix} P_{H11} & P_{H12} & P_H \end{bmatrix},$$

$$P_{H11} := \left(\begin{bmatrix} A_H & B_H \\ C_H e^{A_H(h-\eta-\sigma)} & 0 \end{bmatrix}, I, 0 \right),$$

$$P_{H12} := \left(\begin{bmatrix} A_H & B_H \\ C_H e^{A_H(h-\eta)} & 0 \end{bmatrix}, I, 0 \right)$$

$$\tilde{\mathscr{I}}_{H1} := \begin{bmatrix} \mathscr{I}_0 & 0 \\ -\mathscr{I}_\sigma & 0 \\ 0 & \mathscr{I}_{\eta+\sigma} \end{bmatrix},$$

and

$$(\mathscr{L}u_c)[k](\theta) = \left(\begin{bmatrix} P_H & P_{H12} \end{bmatrix} \begin{bmatrix} \mathscr{I}_{\eta+\sigma-h} \\ -\mathscr{I}_{\sigma} \end{bmatrix} \right) (\theta)u[k-1]$$



Fig. 3. Sampled-Data Systems with Output Time-Delays

if $\eta + \sigma > h$, respectively.

Hence we can obtain a state-space representation of the lifted system of G in Fig. 2 by

(i)
$$P_H \leftarrow \begin{bmatrix} P_H & P_{H12} \end{bmatrix}$$
 and $\tilde{\mathscr{I}} \leftarrow \begin{bmatrix} \mathscr{I}_{\eta+\sigma-h} \\ -\mathscr{I}_{\sigma} \end{bmatrix}$.
(ii) Obtain $\begin{bmatrix} A & B_1 & B_2 \\ \dot{C}_1 & \breve{D}_{11} & \dot{D}_{12} \\ C_2 & \acute{D}_{21} & D_{22} \end{bmatrix}$ for the setup in Fig. 1 with G_c , S , and H defined by P_H and $\tilde{\mathscr{I}}$.

(iii) A state-space representation of the lifted system of G

is given by
$$\begin{bmatrix} A & B_2 & B_1 & 0 \end{bmatrix} = \dots = \begin{bmatrix} A & B_2 & B_1 & 0 \end{bmatrix}$$

$$\begin{bmatrix} x[k+1] \\ z[k] \\ y[k] \end{bmatrix} = \begin{bmatrix} A & B_2 & B_1 & 0 \\ 0 & 0 & 0 & I \\ \hline \dot{C}_1 & \dot{D}_{12} & \breve{D}_{11} & 0 \\ \hline C_2 & D_{22} & \breve{D}_{21} & 0 \end{bmatrix} \begin{bmatrix} x[k] \\ w[k] \\ u[k] \end{bmatrix}$$

if $\eta + \sigma > h$. We can obtain it for the case of $\eta + \sigma \le h$ similarly.

Note also that we can apply Property 4 for general hold H in (11) and/or general time-delay Σ of the form

$$u_c(t) = \sum_{i=1}^{n_L} M_{Li} v_c(t - \sigma_i)$$

by using the linearity (See Remark 1 also).

3) Output Delays: Consider a sampled-data system with time-delays Σ at measurement output as depicted in Fig. 3. The next property is a key to treat sampled-data systems with output time-delay:

Property 5: Given a sampler *S*: $v_c \mapsto y$:

$$y[k] = \mathscr{S}_{\eta} P_{\mathcal{S}}(\mathscr{L}v_c)[k-1] + E_{\mathcal{S}}v_c(kh)$$

and a time-delay *L*: $y_c \mapsto v_c$:

$$v_c(t) = y_c(t - \boldsymbol{\sigma})$$

where $\eta \in [0, h)$, $\sigma \in (0, h)$. Then $y = S\Sigma y_c$ satisfies

$$y[k] = \begin{bmatrix} \mathscr{S}_h & -\mathscr{S}_{h-\sigma} \end{bmatrix} \begin{bmatrix} P_{S1} \\ P_{S2} \end{bmatrix} (\mathscr{L}y_c)[k-2] \\ + \begin{bmatrix} \mathscr{S}_{\eta-\sigma} & \mathscr{S}_{h-\sigma} \end{bmatrix} \begin{bmatrix} P_S \\ E_S \end{bmatrix} (\mathscr{L}y_c)[k-1]$$

 TABLE I

 Overloading Multiplication Operator (*)

| | ssbc | sampler | impgen | gsampler | ghold | matrix |
|----------|----------|---------|--------|----------|--------|----------|
| ssbc | ssbc | — | ghold | — | ghold | ssbc |
| sampler | gsampler | — | — | — | matrix | sampler |
| impgen | _ | — | — | — | _ | impgen |
| gsampler | gsampler | — | matrix | _ | matrix | gsampler |
| ghold | _ | _ | _ | _ | _ | ghold |
| matrix | ssbc | sampler | impgen | gsampler | ghold | matrix |

if $\eta \geq \sigma$, where

$$P_{S1} := \left(\begin{bmatrix} A_S & B_S \\ C_S e^{A_S(\eta - \sigma)} & 0 \end{bmatrix}, I, 0 \right),$$
$$P_{S2} := \left(\begin{bmatrix} A_S & B_S \\ C_S e^{A_S\eta} & 0 \end{bmatrix}, I, 0 \right),$$

and

$$y[k] = \begin{bmatrix} \mathscr{S}_{h+\eta-\sigma} & -\mathscr{S}_{h-\sigma} \end{bmatrix} \begin{bmatrix} P_S \\ P_{S2} \end{bmatrix} (\mathscr{L}y_c)[k-2] \\ + \mathscr{S}_{h-\sigma}E_S(\mathscr{L}y_c)[k-1]$$

if $\sigma > \eta$, respectively.

Hence we can obtain a state-space representation of the lifted system of G in Fig. 3 by

(i)
$$\tilde{\mathscr{I}}_{S} \leftarrow \begin{bmatrix} \mathscr{S}_{h+\eta-\sigma} & -\mathscr{S}_{h-\sigma} \end{bmatrix}$$
 and $P_{S} \leftarrow \begin{bmatrix} P_{S} \\ P_{S2} \end{bmatrix}$.
(ii) $O_{1} \leftarrow \begin{bmatrix} A & B_{1} & B_{2} \\ O_{2} & O_{2} & O_{2} \end{bmatrix}$ for the set of S is the set of S is the set of S is the set of S is the se

(ii) Obtain $\begin{bmatrix} \dot{C}_1 & \check{D}_{11} & \dot{D}_{12} \\ C_2 & \acute{D}_{21} & D_{22} \end{bmatrix}$ for the setup in Fig. 1 with

 G_c , H, and S defined by P_S and $\tilde{\mathscr{I}}_S$.

(iii) A state-space representation of the lifted system of *G* is given by

$$\begin{bmatrix} x[k+1] \\ z[k] \\ y[k] \end{bmatrix} = \begin{bmatrix} A & 0 & B_1 & B_2 \\ C_2 & 0 & D_{21} & D_{22} \\ \hline C_1 & 0 & D_{11} & D_{12} \\ \hline 0 & I & 0 & 0 \end{bmatrix} \begin{bmatrix} x[k] \\ w[k] \\ u[k] \end{bmatrix}$$

if $\sigma > \eta$. We can obtain it for the case of $\sigma \leq \eta$ similarly.

We can apply Property 5 for general sampler (10) and/or general time-delay Σ of the form:

$$v_c(t) = \sum_{i=1}^{n_L} M_{Li} y_c(t - \sigma_i)$$

by using the linearity (See Remark 1 also).

IV. IMPLEMENTATION ON MATLAB

A. Object-Oriented Programming

The main motivation of this paper is to implement formulas in operator form as in (1) directly. For the purpose, we need a programming language having the following features:

- (i) One variable can represent an SSBC and linear combination of sample or impulse operators.
- (ii) We can define operators such as +, *, and so on for operation among SSBCs and linear combinations of sample or impulse operators.

These requirements are satisfied by object-oriented programming languages. Among them, we choose MathScript on MATLAB to utilize the existing class definitions for CACSD.

1) Class Definitions: The following classes are defined in the developed library:

- (i) ssbc: An object of the ssbc class represents an SSBC. This class is implemented as a subclass of ss in Control System Toolbox.
- (ii) sampler: An object of the sampler class represents a linear combination of sample operators in (9).
- (iii) impgen: An object of the impgen class represents a linear combination of impulse operators in (9).
- (iv) gsampler: An object of the gsampler class represents a product of a linear combination of sample operators in (9) and an SSBC. For example, an object of the gsampler class can represent the operator B_1 in (2).
- (v) ghold: An object of the ghold class represents a product of an SSBC and a linear combination of impulse operators in (9). For example, an object of the ghold class can represent the operator \tilde{C}_1 in (2).

2) *Operator Overloading:* The operator overloading feature enhances the readability, and it is a great help for easy development and maintenance.

In the developed library, operators for horizontal concatenation ([a, b]), vertical concatenation ([a; b]), addition, subtraction, multiplication, and transpose (a') are overloaded. Operands for operators other than multiplication and transpose must be objects of the same class, and the results is also an object of the same class.

Table I describes order of operands and the result of the overloaded multiplication operator. We can see that, for example, the result of multiplication of an ssbc and an impgen objects is a ghold object. The symbol – means that the operation is undefined for the related order of the operands.

| operand | result | |
|----------|----------|--|
| ssbc | ssbc | |
| sampler | impgen | |
| impgen | sampler | |
| gsampler | ghold | |
| ghold | gsampler | |

Classes of the operand and the result of the overloaded transpose operator are summarized as follows:

B. Examples

In this subsection, we demonstrate how the readability is enhanced by using the developed library.

Consider the sampled-data system in Fig. 1 and assume the ideal sampler and the ZOH. In the case we can implement (4) instead of (1). However, we can implement (1) by using the developed library with enhancement of readability.

We can represent D_{11} in (2) operator by an ssbc operator D11 by the following syntax:

```
D11tmp = ss(Ac, Bc1, Cc1, Dc11);
D11 = ssbc(D11tmp, h);
```

Similarly D_{12} operator is represented by an ghold object D12 defined as

```
D12tmp = ss(Ac, Bc2, Cc1, Dc12);
D12op = ssbc(D12tmp, h);
Sint = ss(0, 1, 1, 0); % integrater
Sint = ssbc(Sint, h);
H = Sint*impgen(0);
D12 = D12op*H;
```

where H represents the ZOH. Finally the formula (1) is implemented as follows:

```
P = append(B1, [C1, D12]');
Q = [eye(mc1), -D11'; ...
-D11, eye(pc1)];
R = [zeros(n), A, B2; ...
[A, B2]', zeros(n+m2)] + P/Q*P';
```

where append() is the function for block diagonal augmentation, and R is a matrix and the result of the RHS of (1). We confirm that readability of the program with the developed library. We also emphasize that the last syntax is applicable even if S is not an ideal sampler and/or H is not a ZOH just by changing the definitions of variables.

V. CONCLUDING REMARKS

We have implemented a library for SSBCs on MATLAB for development of a CACSD package for sampled-data systems. We have shown that we can handle a wide variety of setups of sampled-data systems including multirate control and input/output time-delay in a unified way by using the SSBC representation. Based on the developed library with object-oriented programming feature, it is easy to implement analysis and design algorithms of sampled-data systems with high readability, and a CACSD package for analysis and design of sampled-data system is under development.

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Appendix

PROOF OF PROPERTY 3

Suppose that (5) is well-posed. From [8], y is given by

$$\mathbf{y}(t) = \int_0^h K(t, s) u(s) \,\mathrm{d}s + Du(t)$$

where

$$K(t,s) := \begin{cases} C e^{At} \Xi^{-1} \Omega e^{-As} B & 0 \le s < t \le h \\ -C e^{At} \Xi^{-1} \Upsilon e^{A(h-s)} B & 0 \le t < s \le h \end{cases}$$

Hence Property 3 follows.

REFERENCES

- [1] B. Bamieh and J. B. Pearson, "A general framework for linear periodic systems with application to \mathscr{H}^{∞} sampled-data control," IEEE Trans. on Automat. Contr., vol. 37, no. 4, pp. 418–435, 1992.
- [2] T. Chen and B. A. Francis: Optimal Sampled-Data Control Systems, Springer, 1995.
- [3] H. Fujioka, Y. Yamamoto, and S. Hara, "Sampled-Data Control Toolbox: A software package via objectoriented programming," in Proc. of 1999 IEEE Int. Symp. on CACSD, pp. 404–409, 1999.
- [4] S. Hara, H. Fujioka, and P. T. Kabamba, "A hybrid state-space approach to sampled-data feedback control," Linear Algebra and Its Applications, vol. 205–206, pp. 675–712, 1994.
- [5] S. Hara, Y. Yamamoto, H. Fujioka, and A. Takeda, "*H*SYS Module Manual," Sumisho Electronics, Co., 1994.
- [6] Y. Hayakawa, S. Hara, and Y. Yamamoto: "*H*_∞ type problem for sampled-data control systems — A solution via minimum energy characterization," IEEE Trans. Automat. Contr., vol. 39, pp. 2278–2284, 1994.
- [7] P. T. Kabamba and S. Hara: "Worst case analysis and design of sampled data control systems," IEEE Trans. Automat. Contr., vol. 38, pp. 1337–1357, 1993.
- [8] L. Mirkin and Z. J. Palmor, "A new representation of the parameters of lifted systems," IEEE Trans. Automat. Contr., vol. 44, pp. 833–840, 1999.
- [9] Y. Yamamoto: "A function space approach to sampleddata control systems and tracking problems," IEEE Trans. on Automat. Contr., vol. 39, pp. 703–712, 1994.