
UTRC Perspective on Controls: Integration for Commercial & Critical Infrastructure

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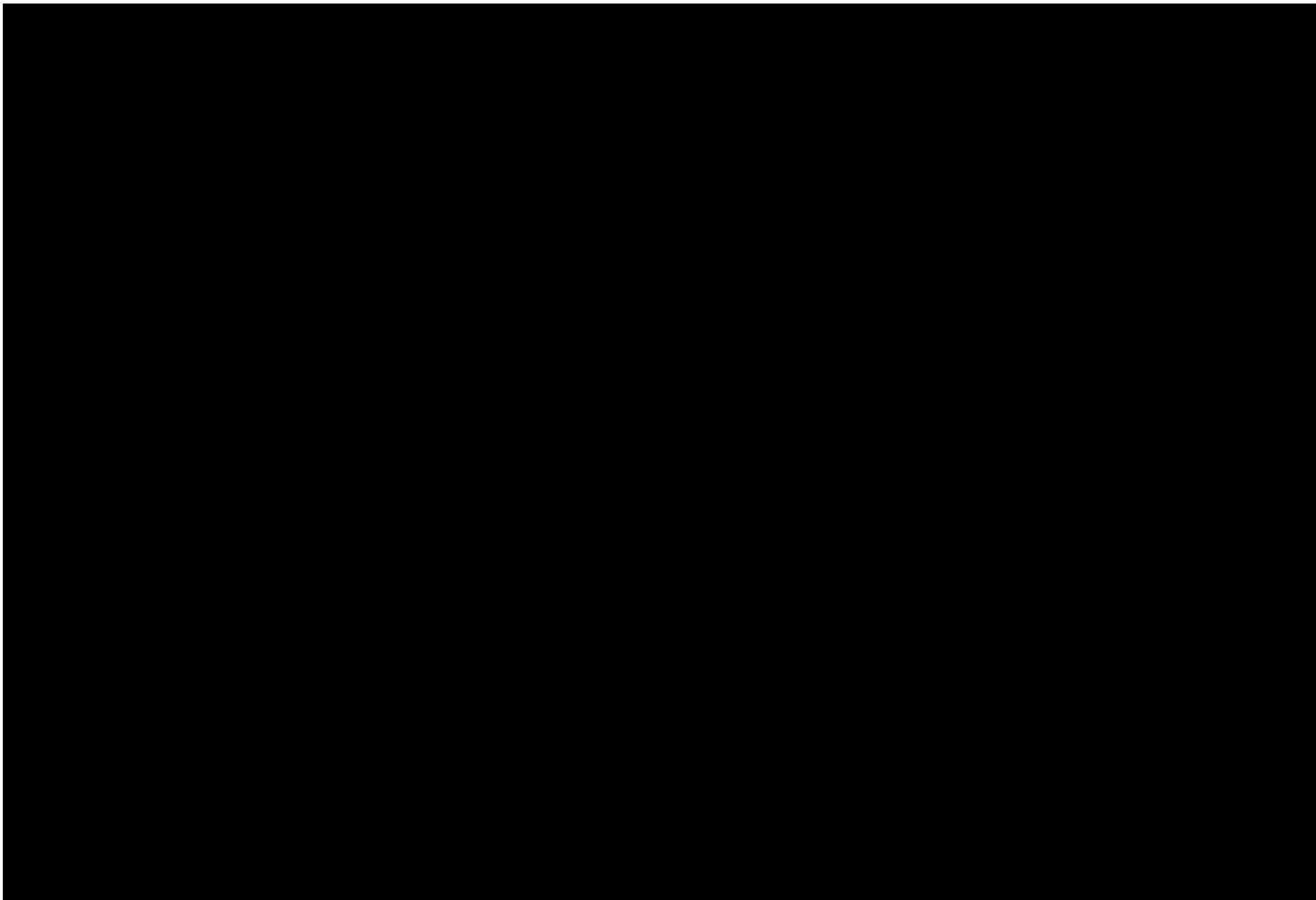
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Outline

- **UTC / UTRC overview**
 - **The Challenge: Integration Through Controls**
 - **The IBECS Example**
 - **From Requirements and Modeling to Implementation**
 - **Conclusion: Innovation Across the Design Cycle**
-

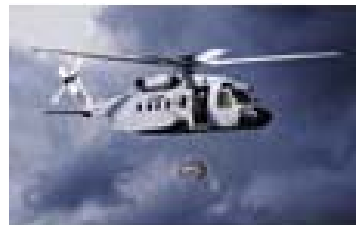
UTC: This is Momentum



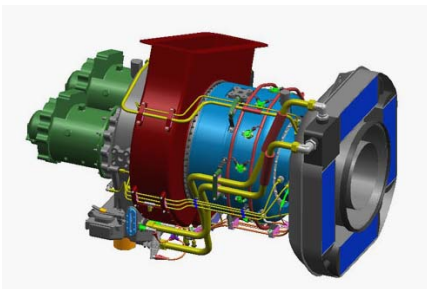
United Technologies Corporation



Pratt & Whitney



Sikorsky



Hamilton-Sundstrand

UTC Power



Carrier



- Aerospace Systems
- Power Systems
- Building Systems

Otis



**UTC Fire & Security
(Kidde & Chubb)**



United Technologies Corporation

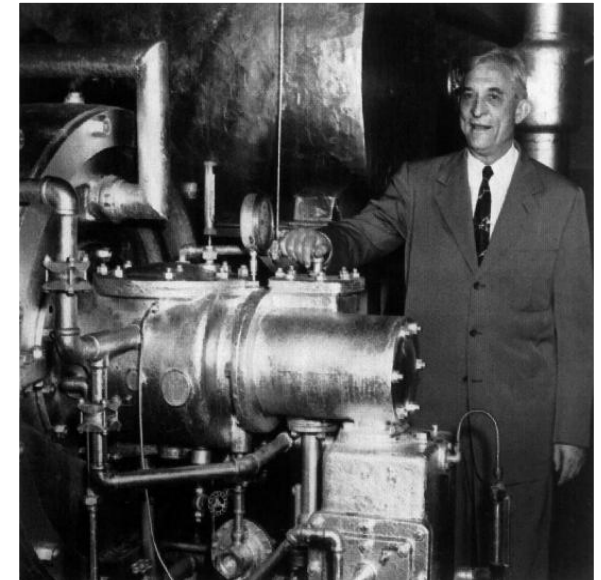
A History of Innovation



Frank Rentschler



Thomas Hamilton



Willis Carrier



Elisha Otis

Igor Sikorsky



United Technologies Research Center

R&D Facilities

UTRC Departments:

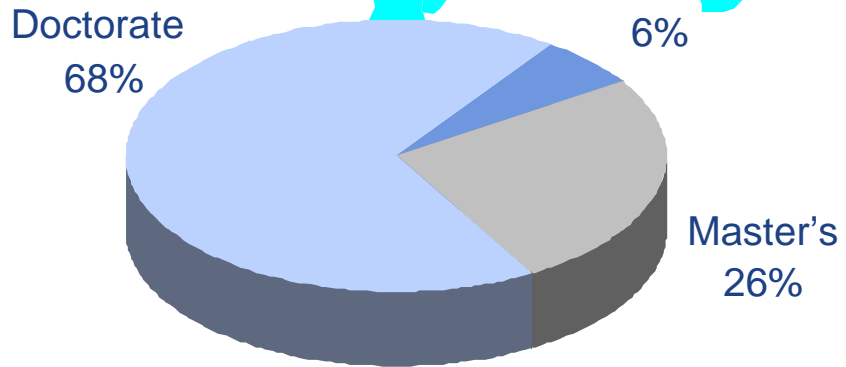
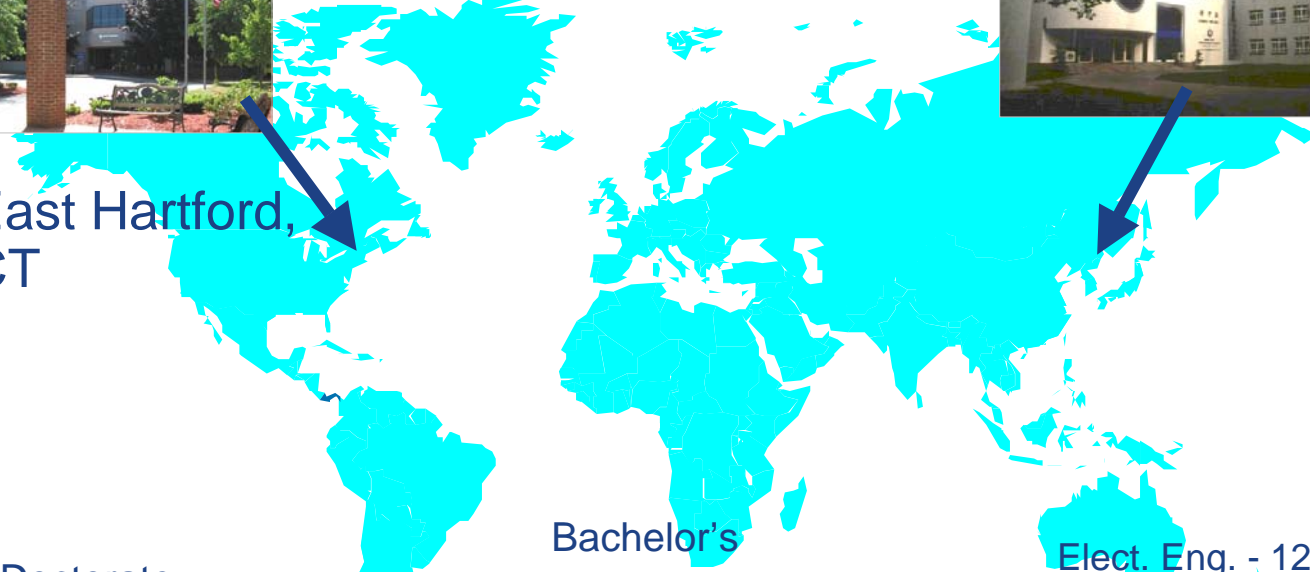
- Systems
- Components
- Physical Sciences
- Product Qualification & Innovation Support



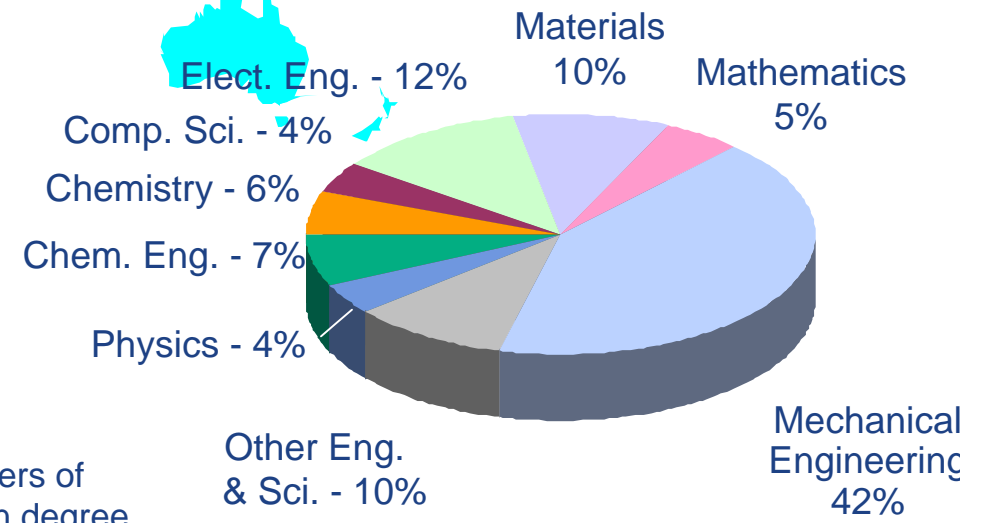
East Hartford, CT



Shanghai, China



7% also hold a Masters of Business Administration degree

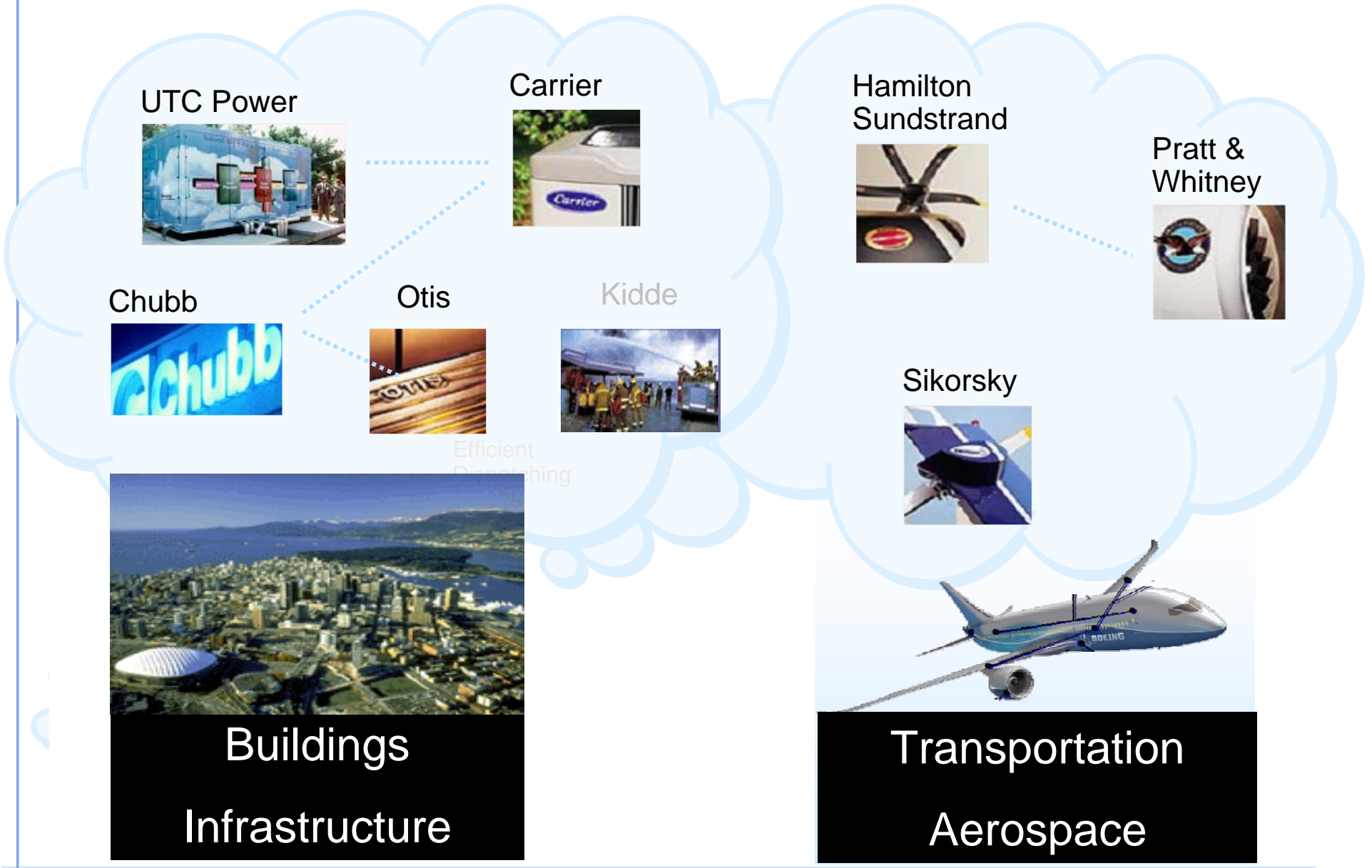


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Challenge - Integrated Product Offerings

Complexity Managed Through Systems Engineering and Supervisory Controls



Key Points

- Innovation in *building system design* and operation is an area of opportunity for *efficiency, safety and comfort*
- *Innovation* can be found in *integration*
- Progress in the design of *integrated systems* requires:
 - (1) focus on *modeling and analysis of dynamics and control*;
 - (2) a design methodology and tools for *embedded systems*;
- *Model based systems engineering* is an *enabler* for the design and implementation of ideas

Why Now?

“Fourth broad trend I’ll note is the increased system integration. Products are smarter. Controls play a larger role.... We believe that system integration trends will continue in homes, office and supermarkets”

Geraud Darnis

Carrier President

Remarks made on July 1, 2004

Systems Integration for Infrastructure Automation and Security

Effectively shape the behavior of products and services, which may involve human decision making, through controls and model and data-driven processes.

Enablers

Systems engineering – risk assessments, requirements, critical parameter management

System level modeling – multi-scale modeling, analysis and control design.

Control and Embedded system design tools and processes – speed development time and reduce risk.

Monitoring – wireless sensor networks and video to enable information acquisition and analysis.

Decision Making –enable energy efficiency, comfort and security, and decision support for first responders

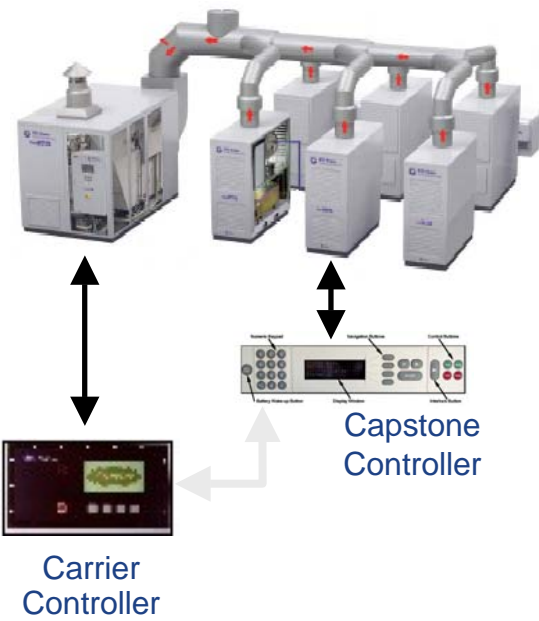


Integrated Cooling, Heating and Power (CHP) Generation

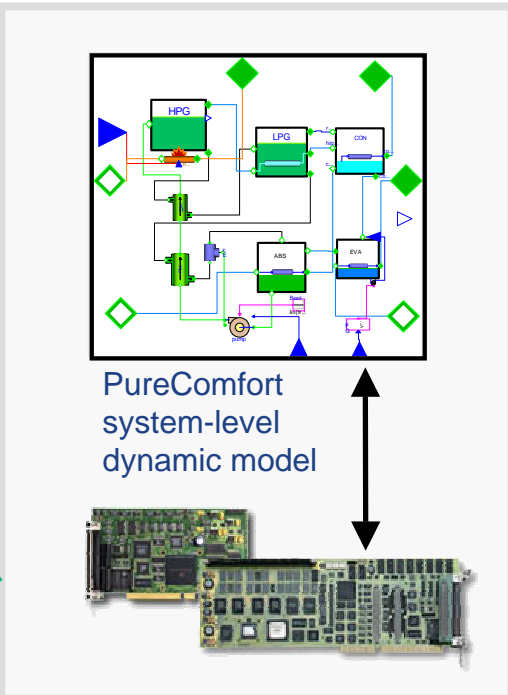
Supervisory Controls Virtual Qualification Speeds Integration

Problem

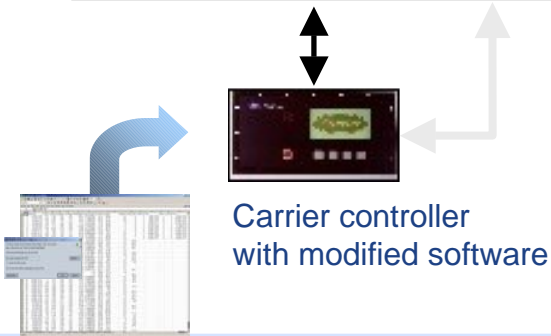
Controller Integration



PureComfort Model



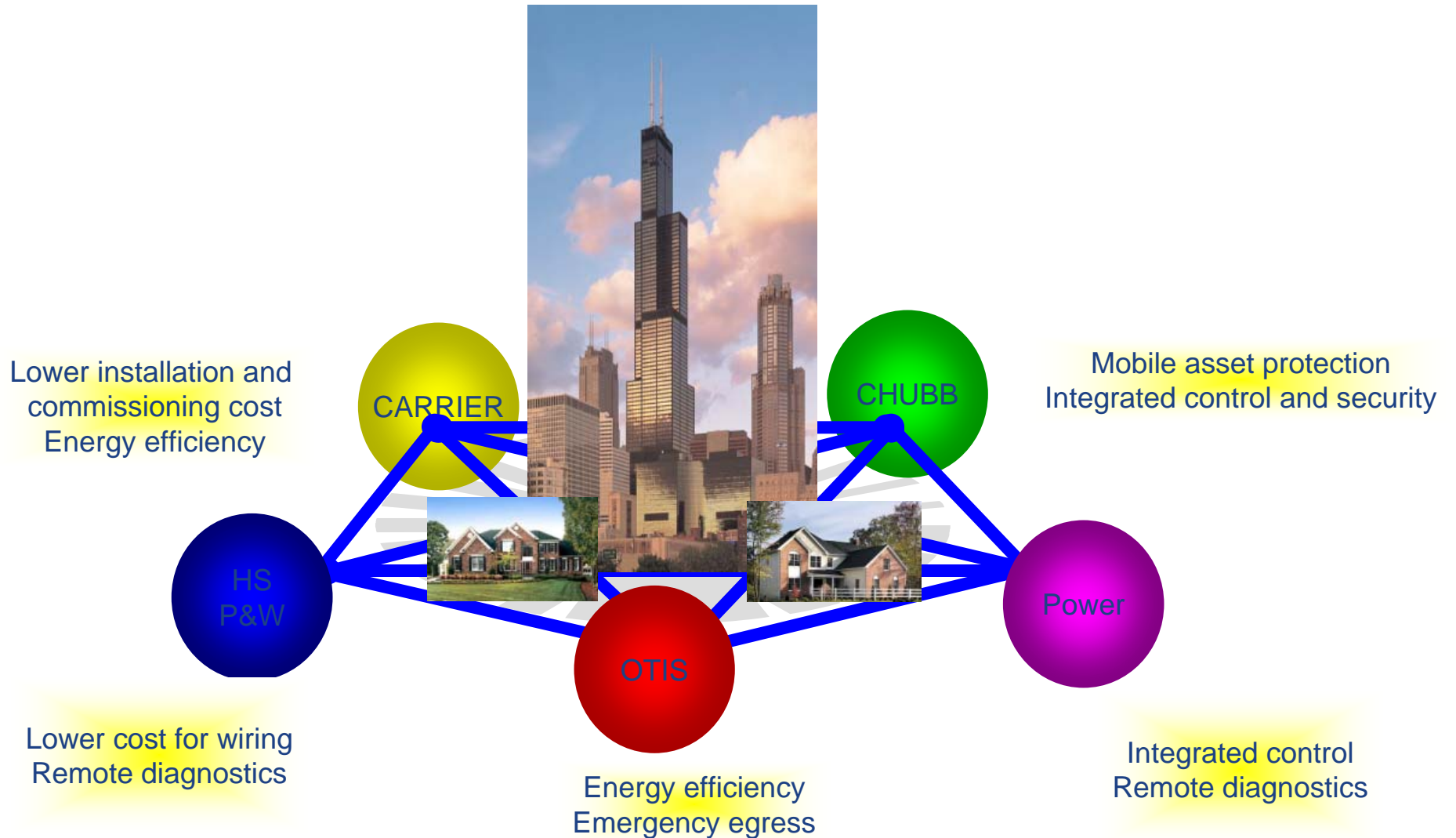
Integrated Solution



- Accelerated Software Qualification 2X
- Risk Reduction – Virtual testing of extreme conditions

Integrated Solutions Through Wireless Sensor Networks (WSN)

WSN enables integration of capabilities



Outline

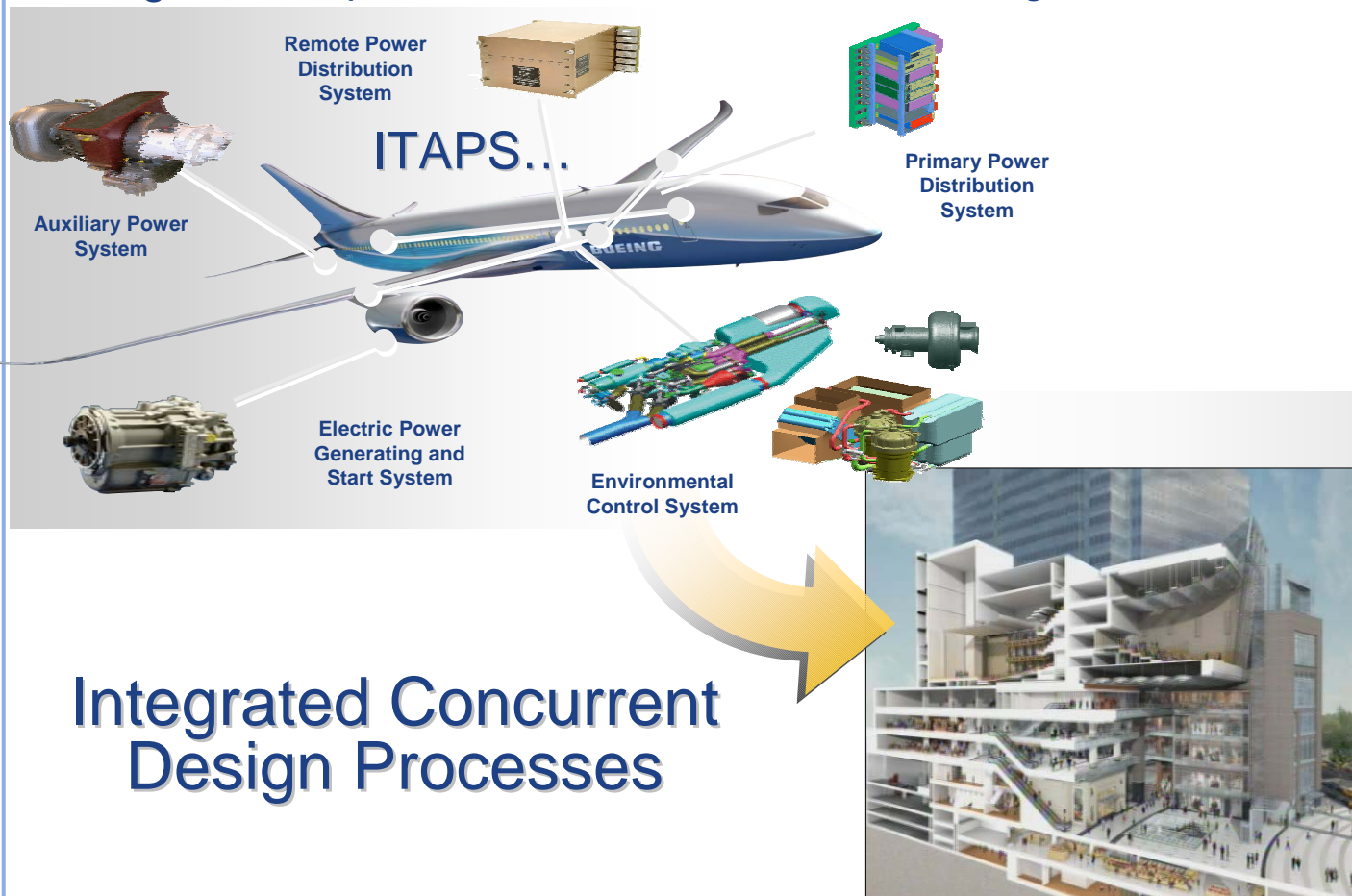
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Integrated Concurrent Design Processes

From ITAPS to IBECS

Integrated Products - A unique offering of which value is significantly higher than the individual components value.

Boeing 7E7 Complete Power, Fuel and Thermal Management Solutions

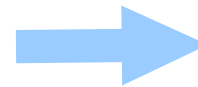


Integrated Design of High Efficiency Commercial Buildings

Current and Desired States

“A model-based systems design methodology to enable development of integrated HVAC and CHP solutions and advanced controls tailored to needs of specific commercial building market segments.”

Current State



Desired State

Design Process

- Build and test, sequential.
- Component - oriented
- Rated, steady state “design point” performance

- Model-based, concurrent.
- Systems - oriented
- Performance quantified throughout dynamic envelope.

Controls

- Proprietary scripting language – multiple versions.
- Proprietary *physical layers (CCN)*
- Sequential development process
- Qualified only at commissioning time

- Model-based, industry-standard best-in-class tools.
- Proprietary *behaviors and algorithms.*
- Concurrent development process.
- Qualify over entire design cycle.

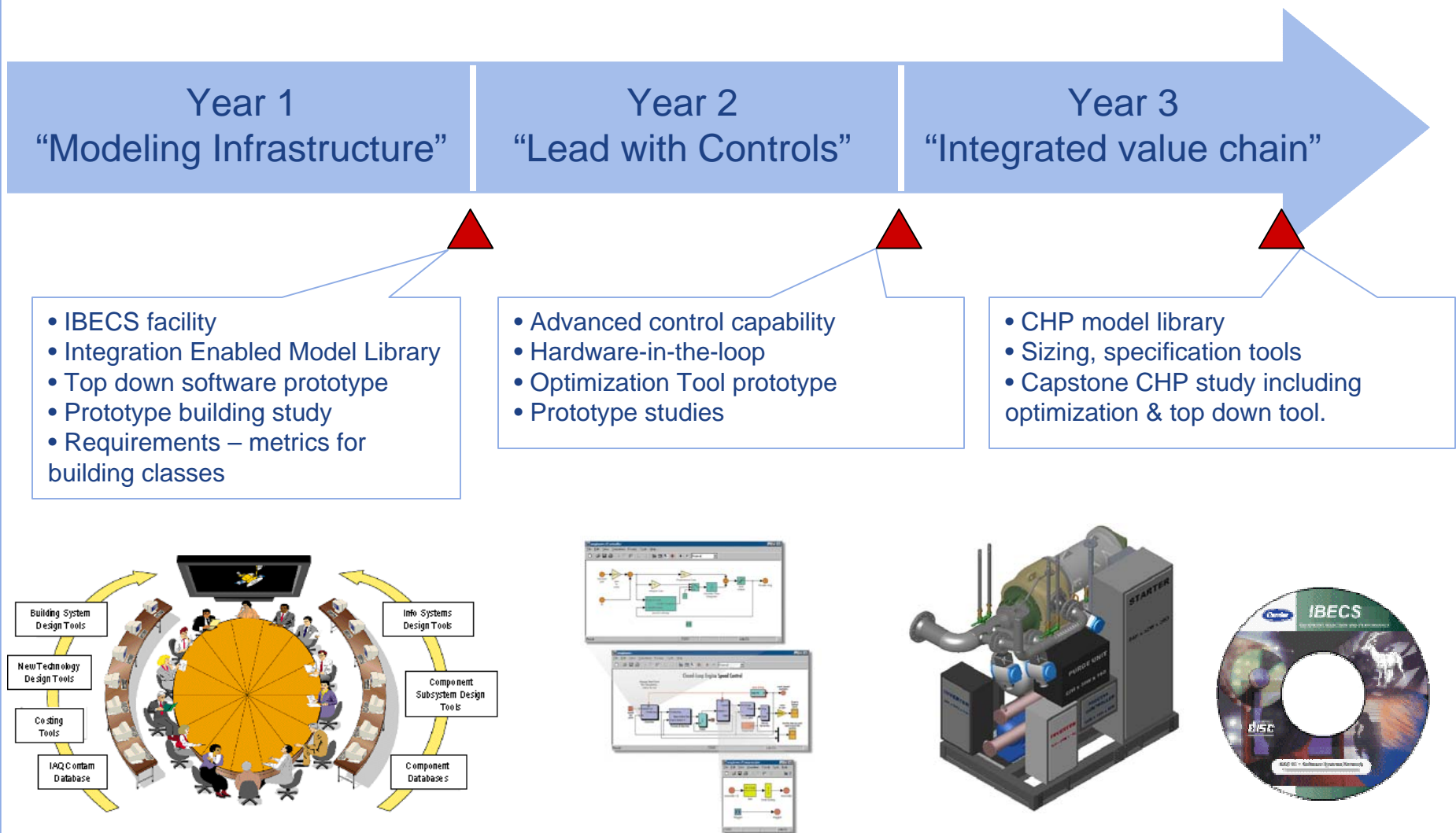
Sizing & Specification Tools

- Component oriented
- Rated “design point” metrics
- Detached from industry-standard tools
- Prescriptive

- Systems oriented
- Operating envelope metrics
- Integrated into emerging construction industry standards
- Proscriptive

Integrated Building Energy & Control Systems (IBECS)

IBECS -> NIST-funded project

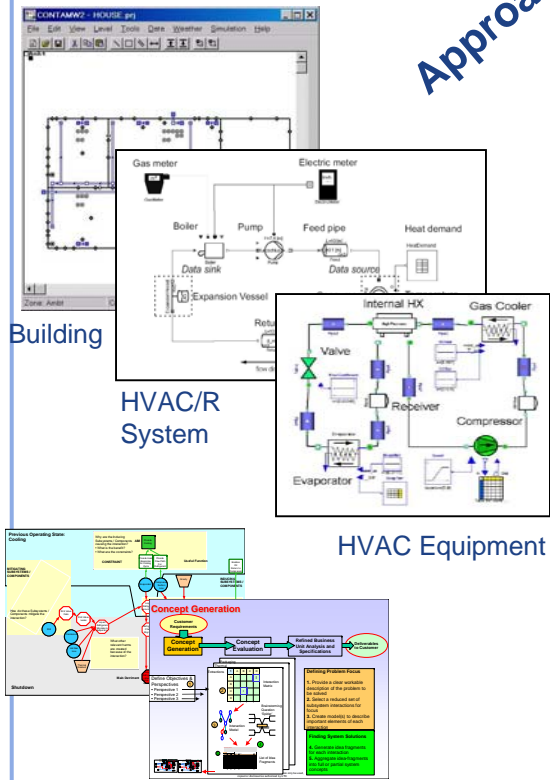


Integrated Building Energy & Control Systems (IBECS)

A model-based systems design methodology for development of integrated HVAC/R and CHP solutions and advanced controls tailored to commercial building market segments.

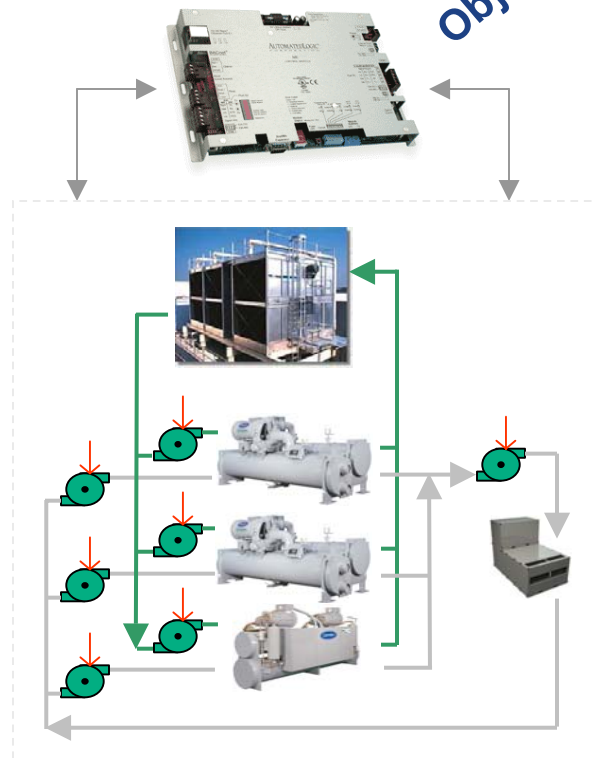
System-Level Model Hierarchy

Approach



Optimal Control

Objectives



Commercial building segments

Scope



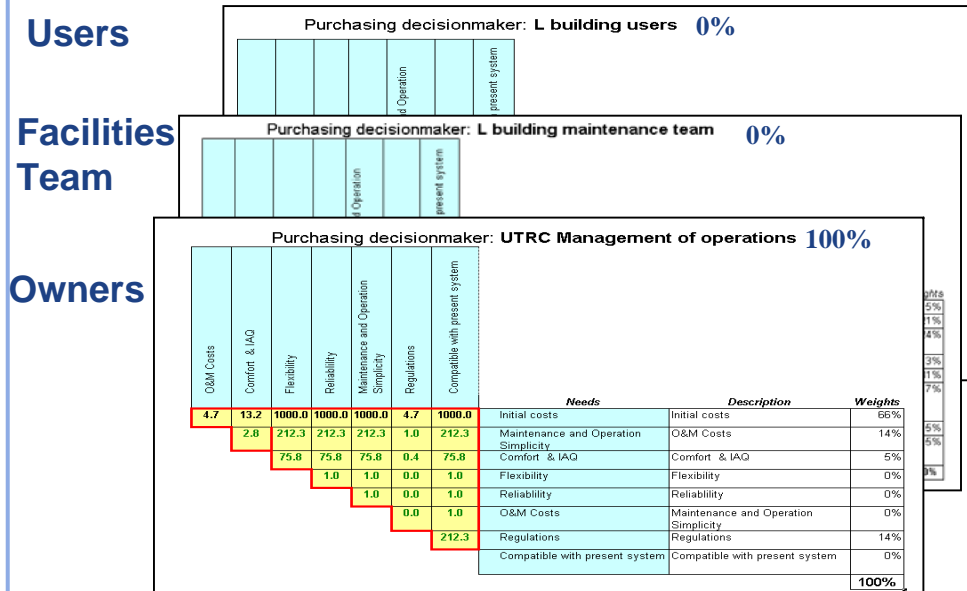
Systems Engineering Process

Optimal Configuration

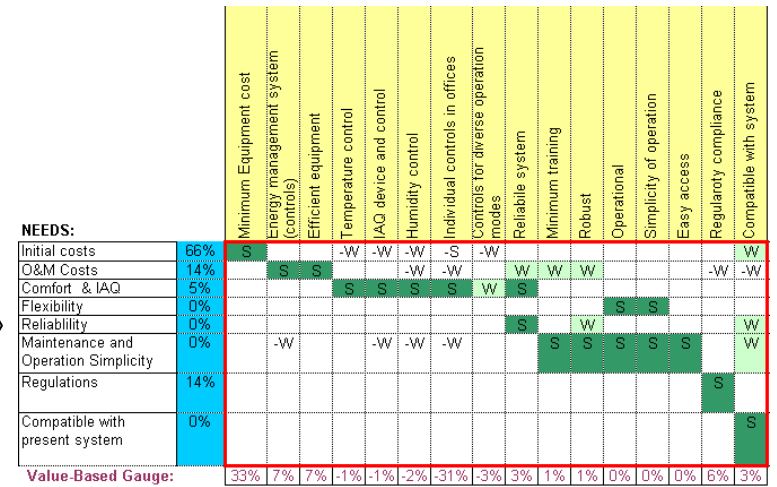
Requirements

Value-Based Gauge

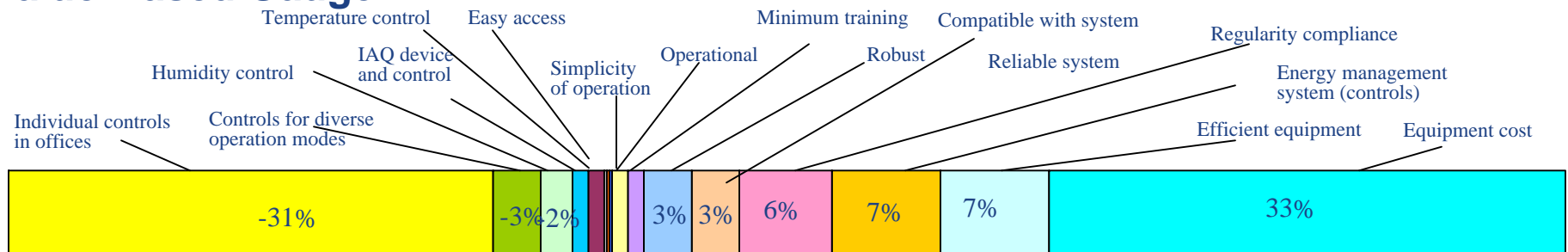
Weighted customer needs for stakeholders



Concept screening matrix: Requirements vs. Characteristics



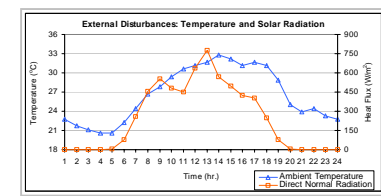
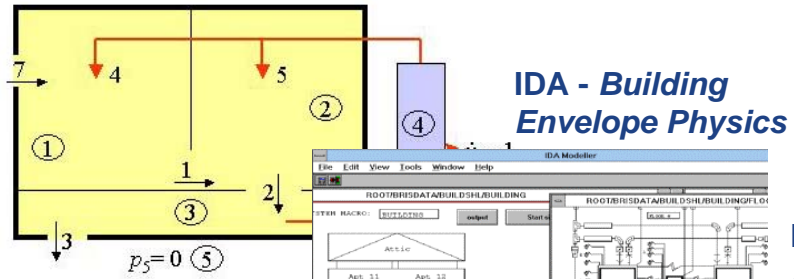
Value-Based Gauge



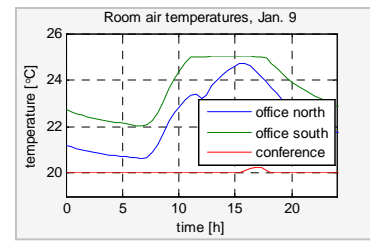
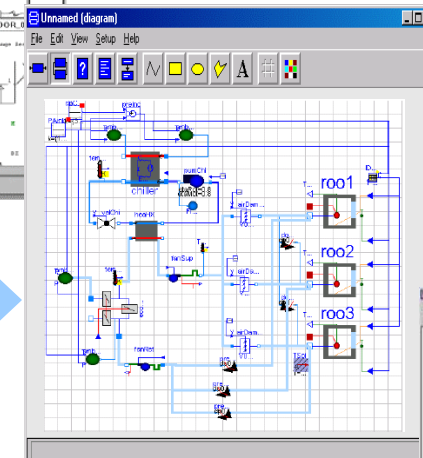
The IBECS Integrated Tool Chain

Dynamic modeling & economic analysis of integrated HVAC/R solutions

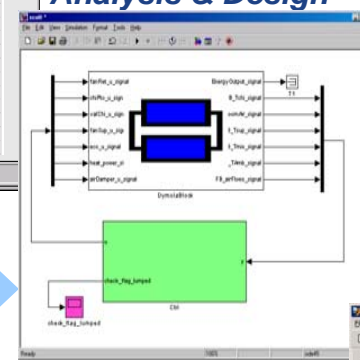
Physical Description



Dymola - HVAC & Airflow Performance



Simulink - Controls Analysis & Design



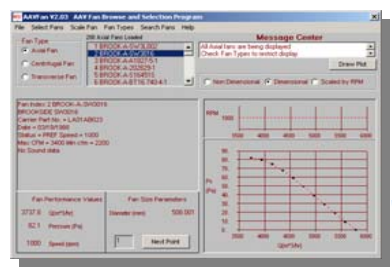
Matlab - Economic Analysis

```

% Run the MATLAB script
load('data.mat');
% Create the plant model
% Create the control system
% Simulate the system
% Plot the results
% Calculate the economic cost
% Optimize the control parameters
% Save the results

```

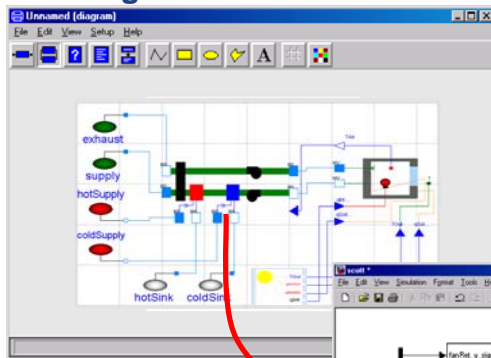
AVVFan Database



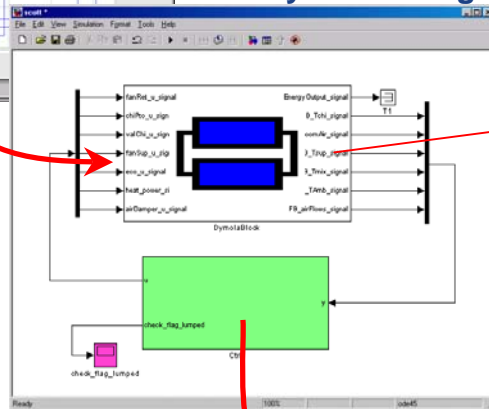
Envisioned IBECS Integrated Tool Chain

Automated Code Generation & HIL Simulation

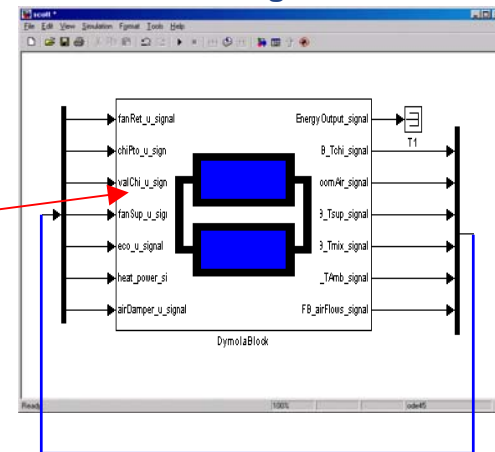
Dymola: HVAC + Building Model



Simulink – Controls Analysis & Design



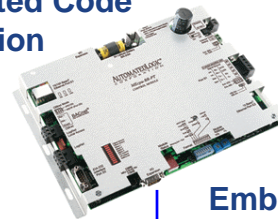
Dymola / Simulink – “Virtual Building”



Hardware-in-the-Loop Simulation

New algorithms

Automated Code Generation



Embedded Platform

Building Management Systems

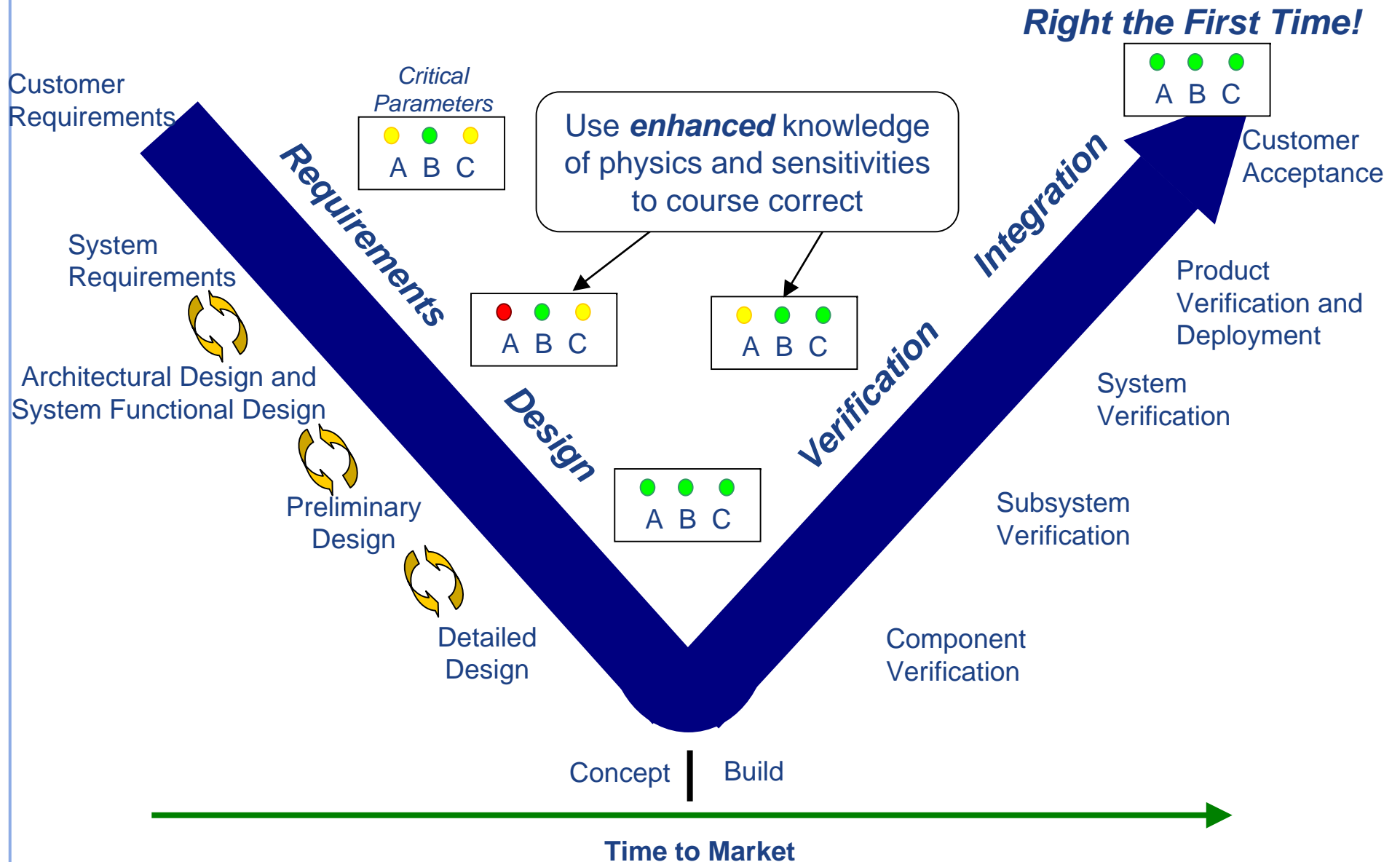


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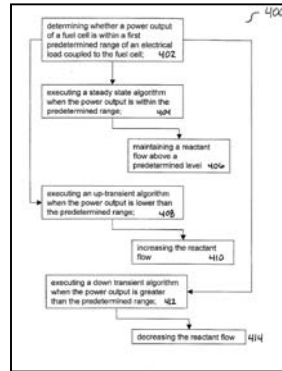
Model-Based Systems Engineering

System Modeling and Analysis in Design Phase enables Robust Solutions



Typical Embedded Controls Opportunity

Address Issues in the Product Development Process



Paper Requirements Documents

Requirements Management

```

00118   for (tok = s;;) {
00119       c = *s++;
00120       spanp = delim;
00121       do {
00122           if ((sc = *spanp++) == c) {
00123               if (c == 0) {
00124                   s = NULL;
00125               } else {
00126                   s[-1] = 0;
00127               }
00128               *stringp = s;
00129               return (tok);
00130           }
00131       } while (sc != 0);
00132   }
00133 }

```

C Code



Circuit Board

Product Quality

Product Differentiation



Monitoring

Avoid Obsolescence



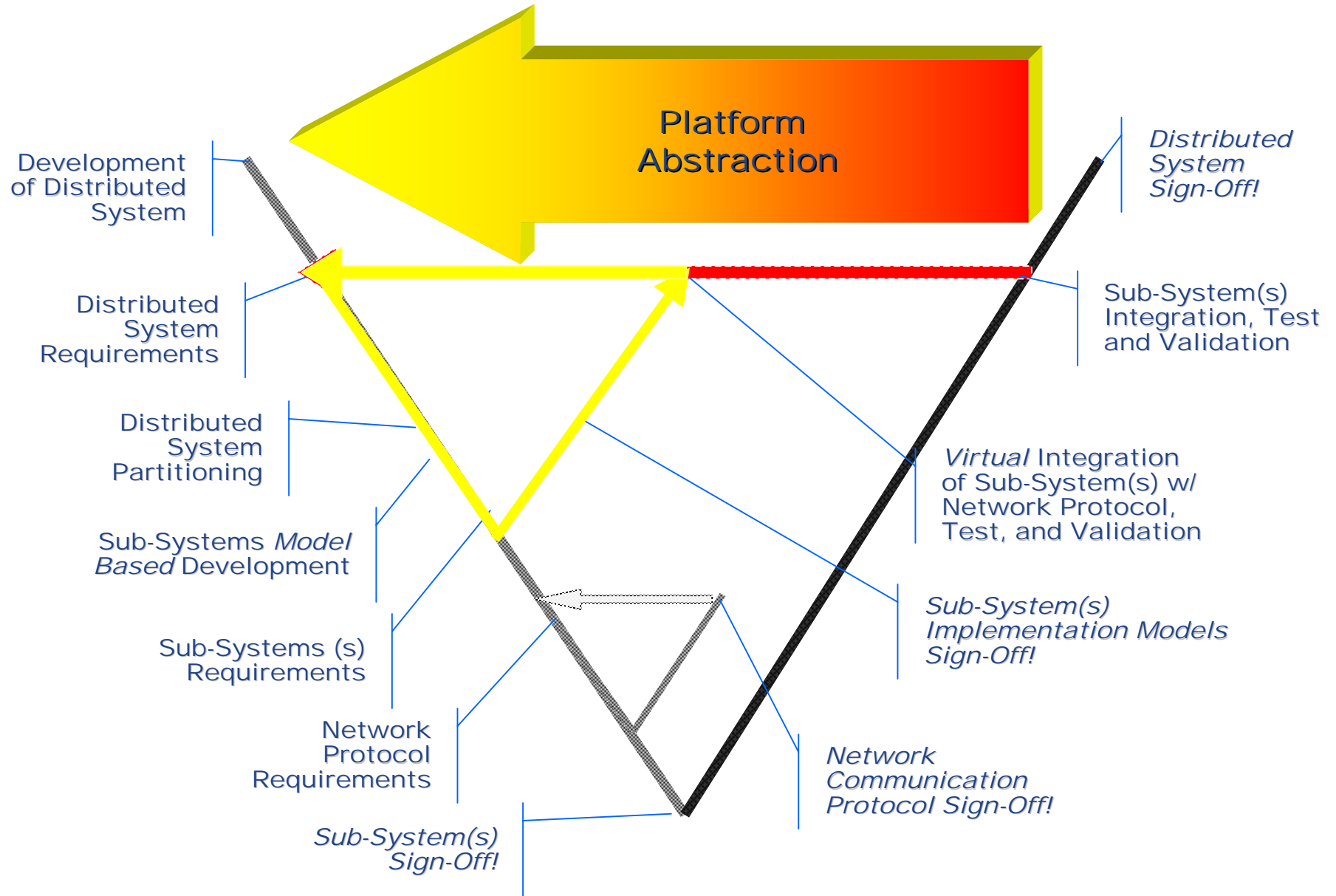
Truck Trailer Container Applications



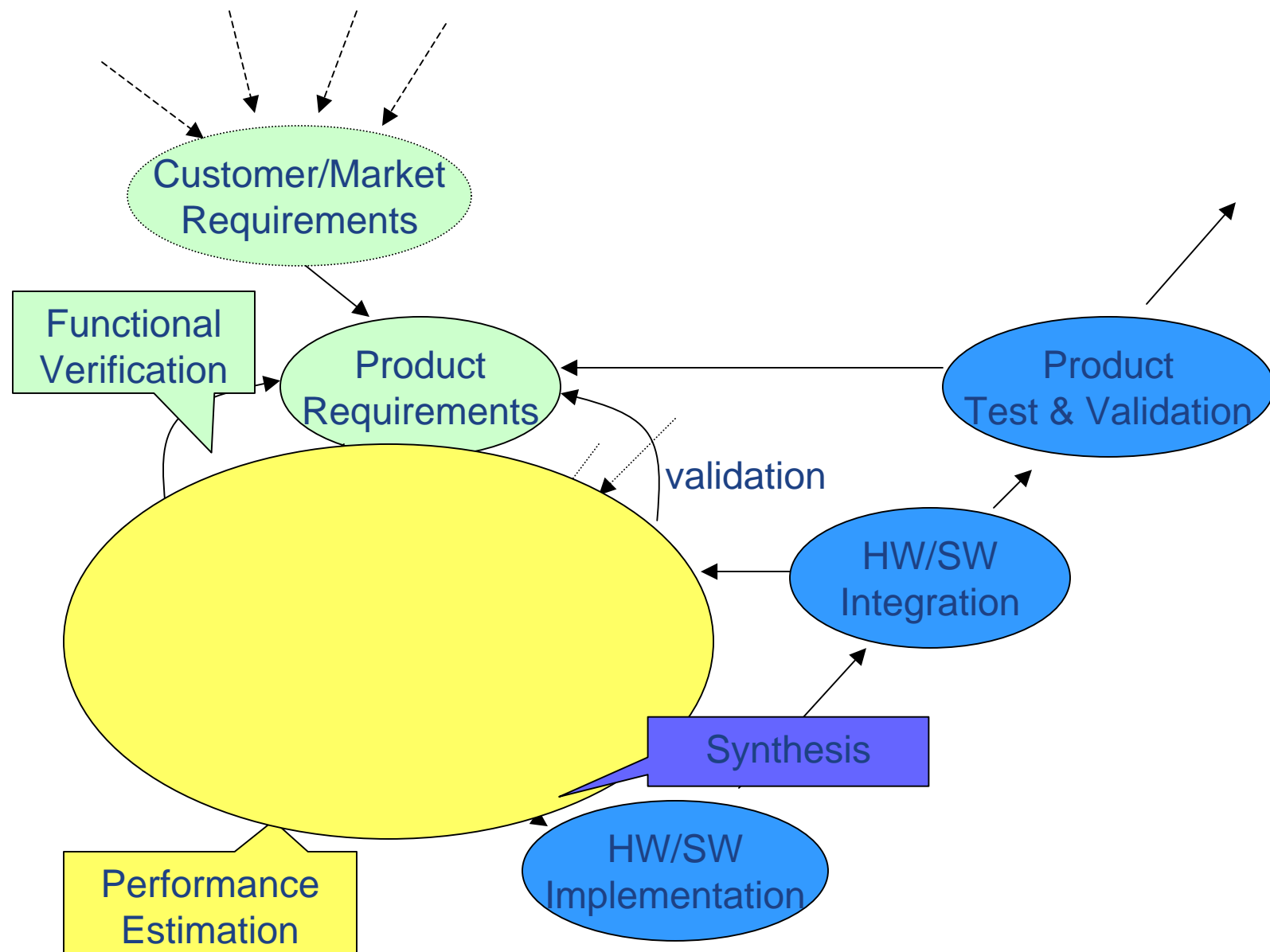
Embedded System

Minimized Rework

Platform Based Design

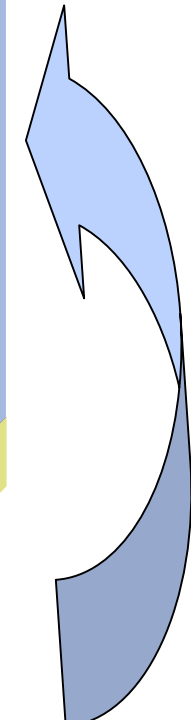
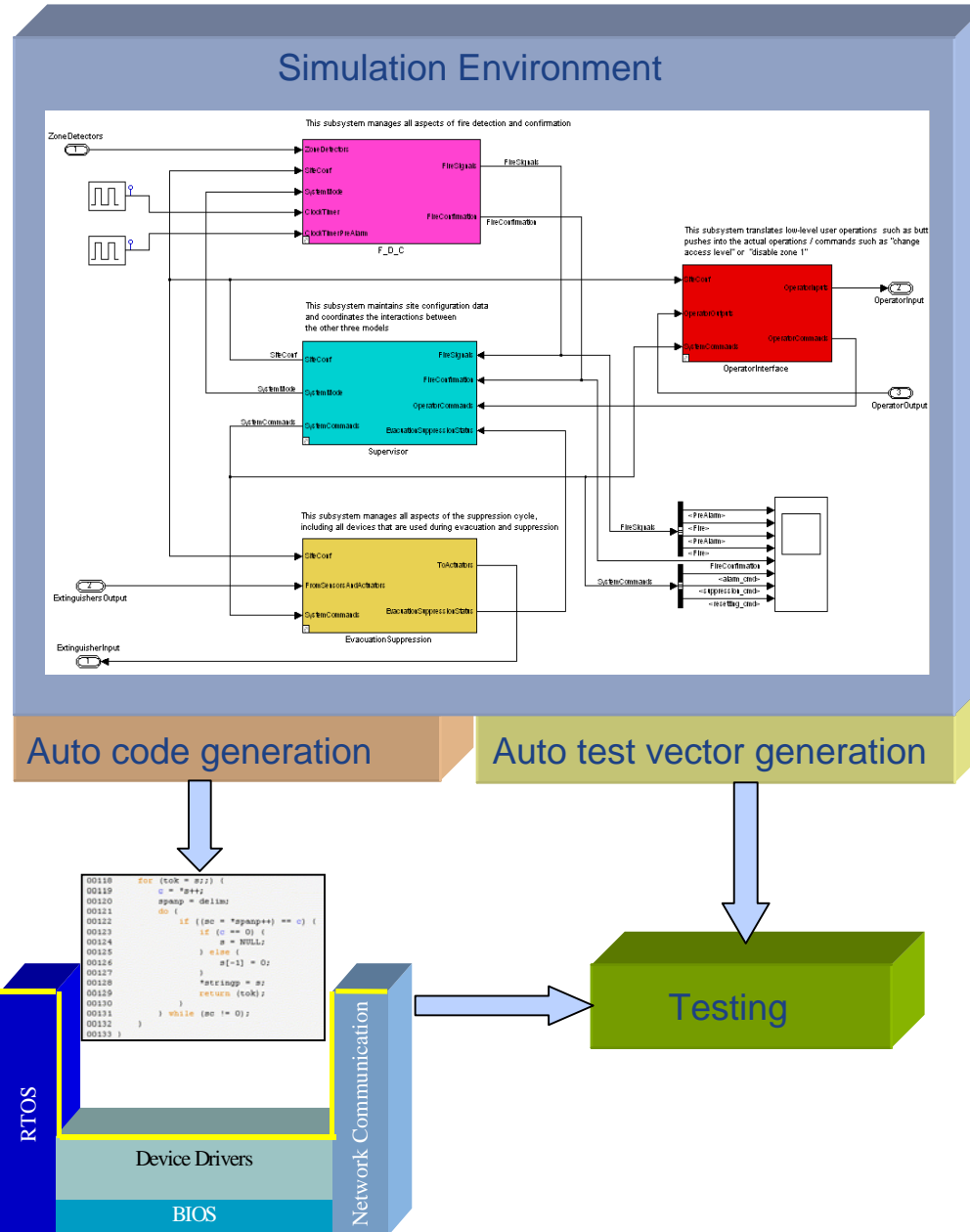


Platform and Model Based Design Methodology



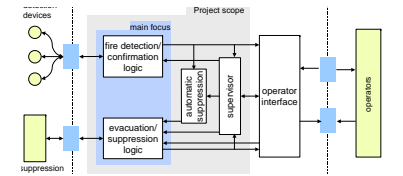
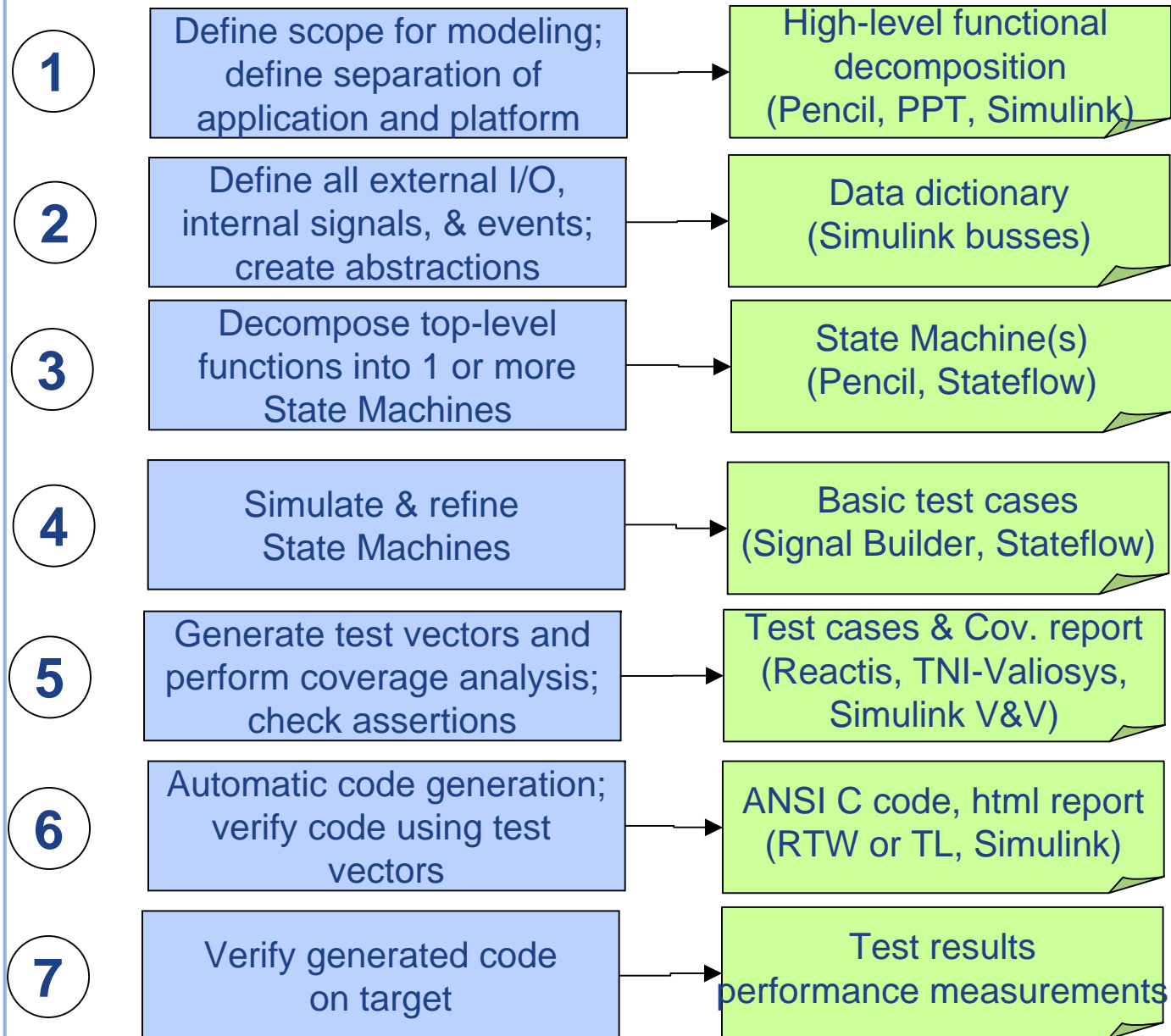
Embedded Control System Modeling, Analysis, Design, Testing

Bird Eye View

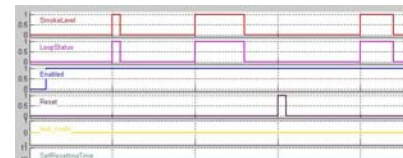
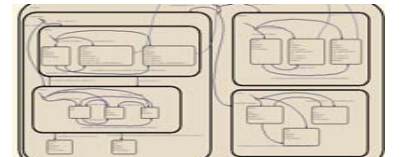


Model-Based Embedded Systems Development

Outline of the Process



General parameters	Type	Range	Default	
Access level for 'log audio signal' command	uint8	0..255	0	Accesses the access level applied for a user
Access level for 'log audio signal' command	uint8	0..255	0	
Type of audio signal used in test mode	uint8	0..255	0	
Number of audio signals used in test mode	uint8	0..255	0	
Delay before audio	uint8	0..255	0	
Delay before audio	uint8	0..255	0	
Audio hardware	uint8	0..255	0	
Printer type	uint8	0..255	0	
Printer type	uint8	0..255	0	
Activation parameters	Type	Range	Default	
Number of parameters	uint8	0..255	0	
IF x = zero self-waiting time	uint8	0..255	0	0 = no self-waiting
IF x = zero pre-activation	uint8	0..255	0	
Suppression parameters	Type	Range	Default	
Suppression delay (evacuation/ignition fault)	uint8	0..255	0	
Suppression delay (audio signal processor fault)	uint8	0..255	0	



Block/Variable/Condition	SI	CI	MSI	CI	MSI	SI	CI	MSI	SI	CI	MSI
1: GET_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
2: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
3: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
4: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
5: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
6: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
7: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
8: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
9: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
10: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
11: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
12: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
13: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
14: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
15: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
16: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
17: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
18: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
19: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
20: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
21: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
22: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
23: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
24: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
25: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
26: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
27: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
28: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
29: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
30: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
31: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
32: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
33: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
34: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
35: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
36: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
37: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
38: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
39: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
40: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
41: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
42: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
43: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
44: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
45: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
46: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
47: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
48: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
49: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%
50: LOG_AUDIO	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%	100%

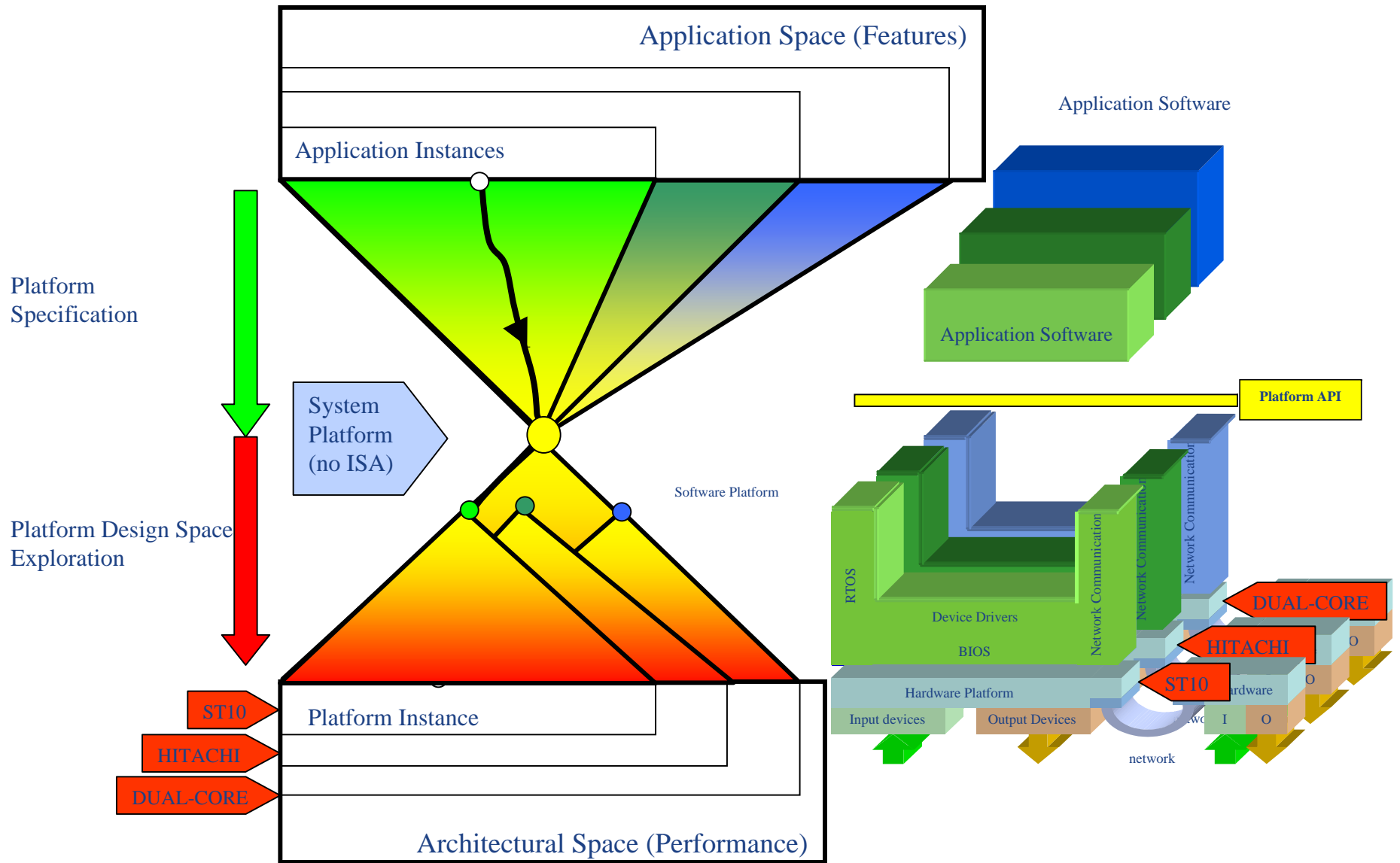
```

00122         if ((sc = *spanp++) == c
00123             if (c == 0) {
00124                 s = NULL;
00125             } else {
00126                 s[-1] = 0;
00127             }
00128             *stringp = s;
00129             return (tok);

```

Platform Based Design Enables Platform Selection

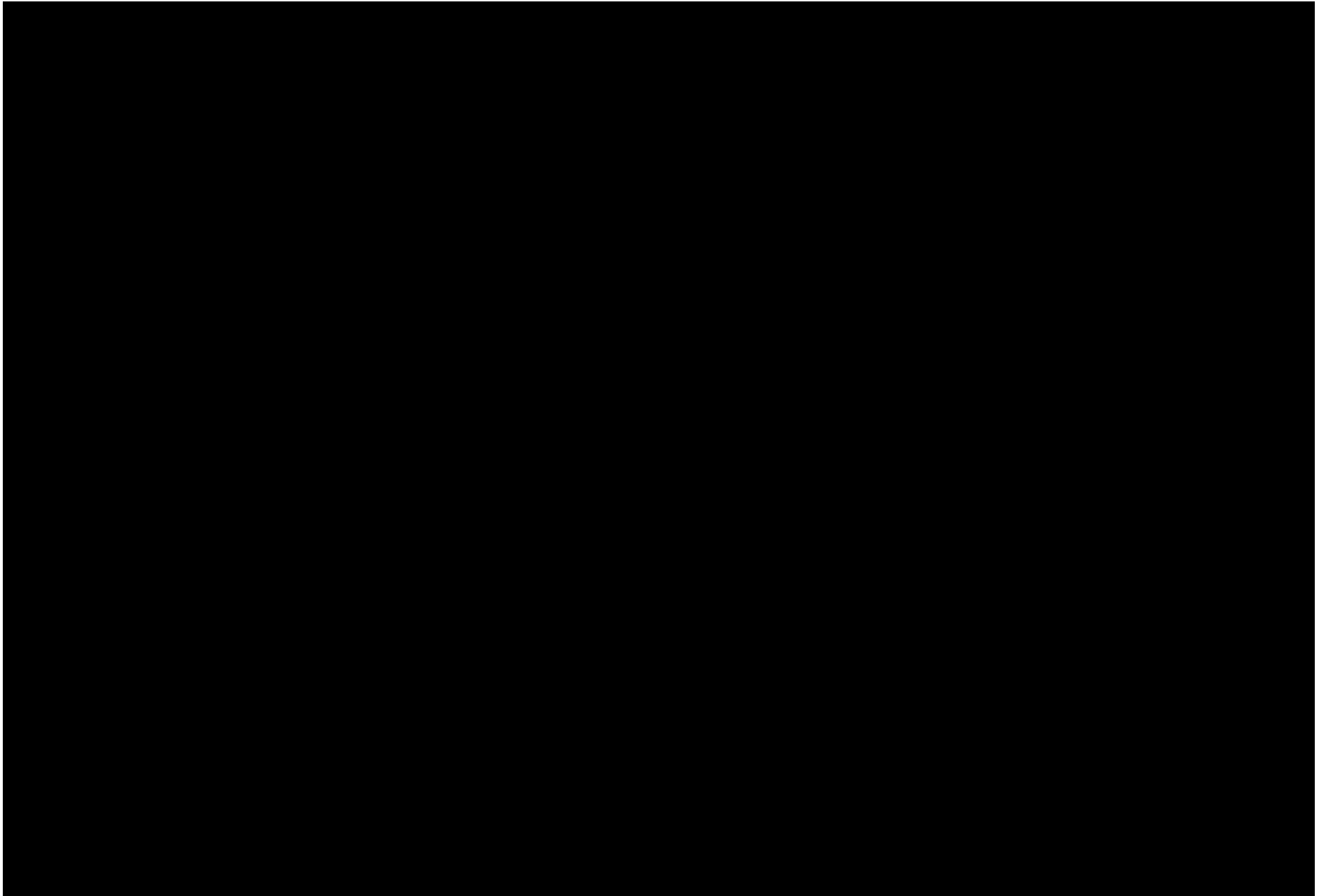
Berkeley and PARADES driven initiatives



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 - **Conclusion: Innovation Across the Design Cycle**
-

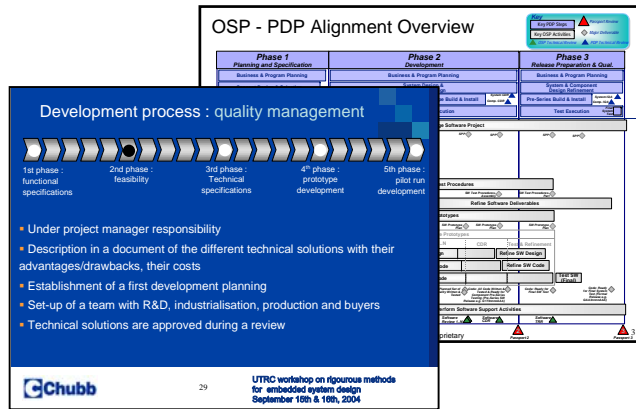
UTRC: Dr. John Cassidy – UTC Senior VP Science & Technology



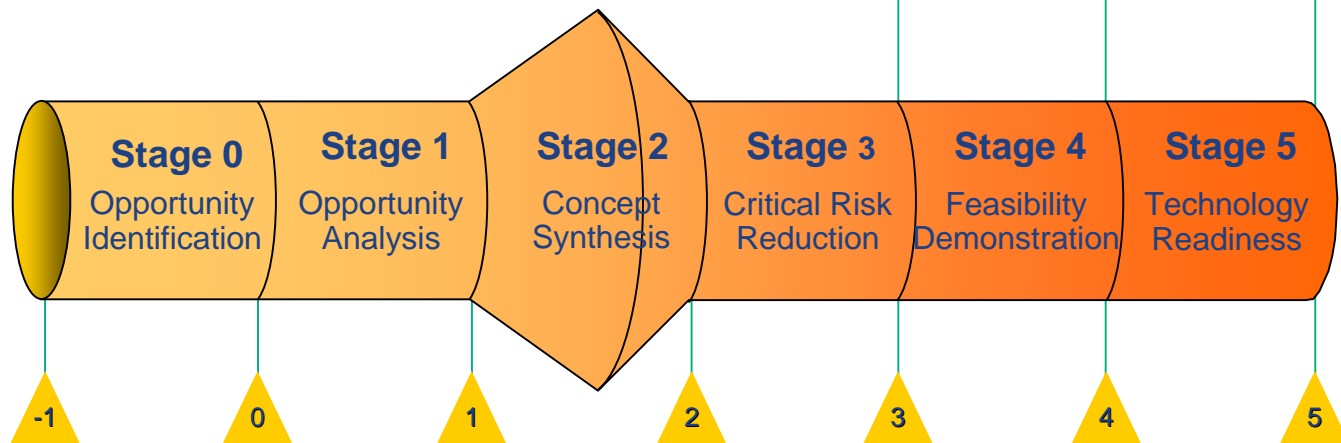
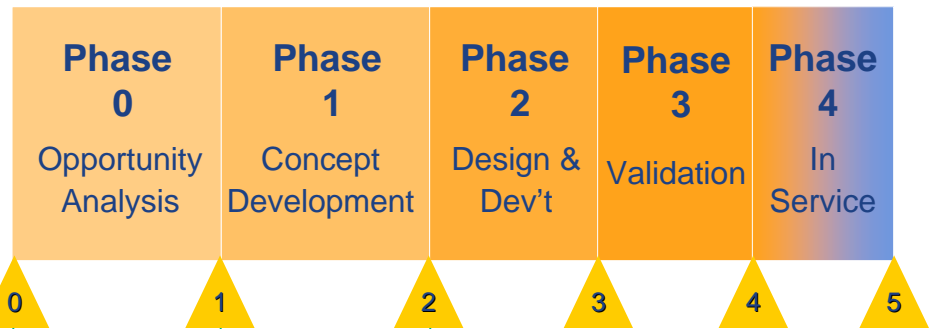
Backup Slides

The Key to Integration: Project Planning and Execution

Achieving Competitive Excellence (ACE) Through Adequate Processes



Business Unit Passport Process



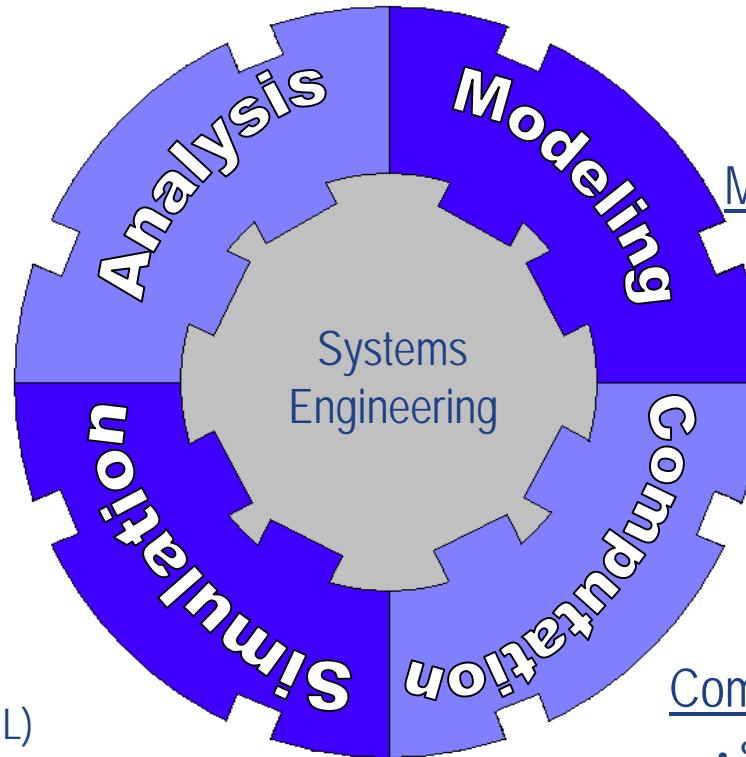
UTRC Project Planning & Execution Process

Modeling, Analysis, Simulation, & Computation (MASC)

Enhancing the UTC Engineering Effectiveness

Analysis

- Dynamical Systems
- Control Design
- Fundamental Limits
- Sensitivity & Uncertainty



Modeling

- Equation-based model construction
- Model verification & validation
- Model maturation

Simulation

- Hardware-in-Loop (HIL)
- Embedded Systems
- Shared, Distributed Computation
- Protected External Participation

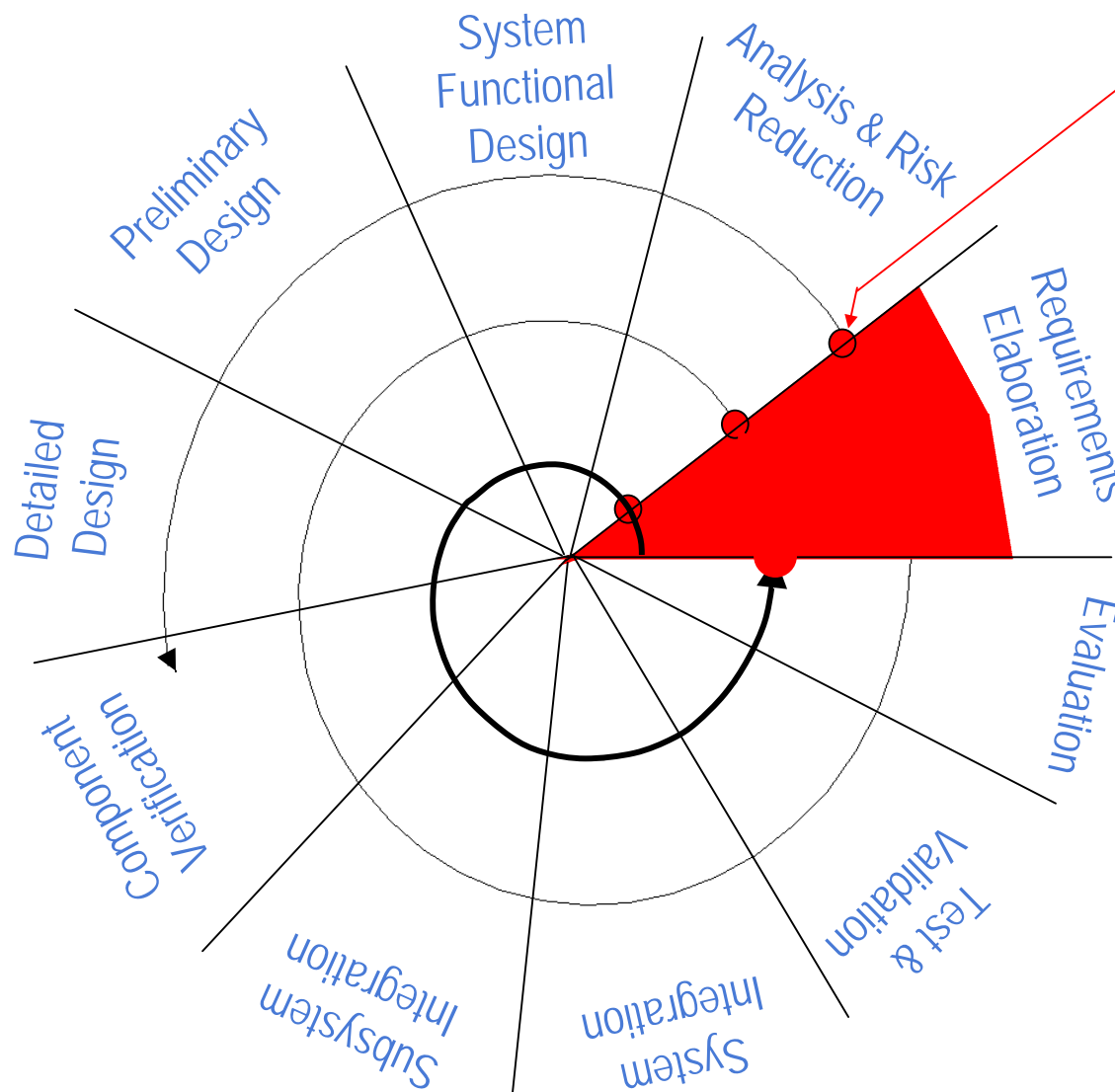
Computation

- Solution Architecture Design
- Multi-scale, multi-fidelity integration
- Multi-Domain integration
- Algorithmics & Numerical Analysis

Make United Technologies a world leader in the effective use of modeling and analysis for competitive differentiation of our products and processes, as measured by development time and costs, system performance and robustness, and product quality and reliability.

Model-Based Systems Engineering

Iteration Through Systems Engineering Process For Design Objectives



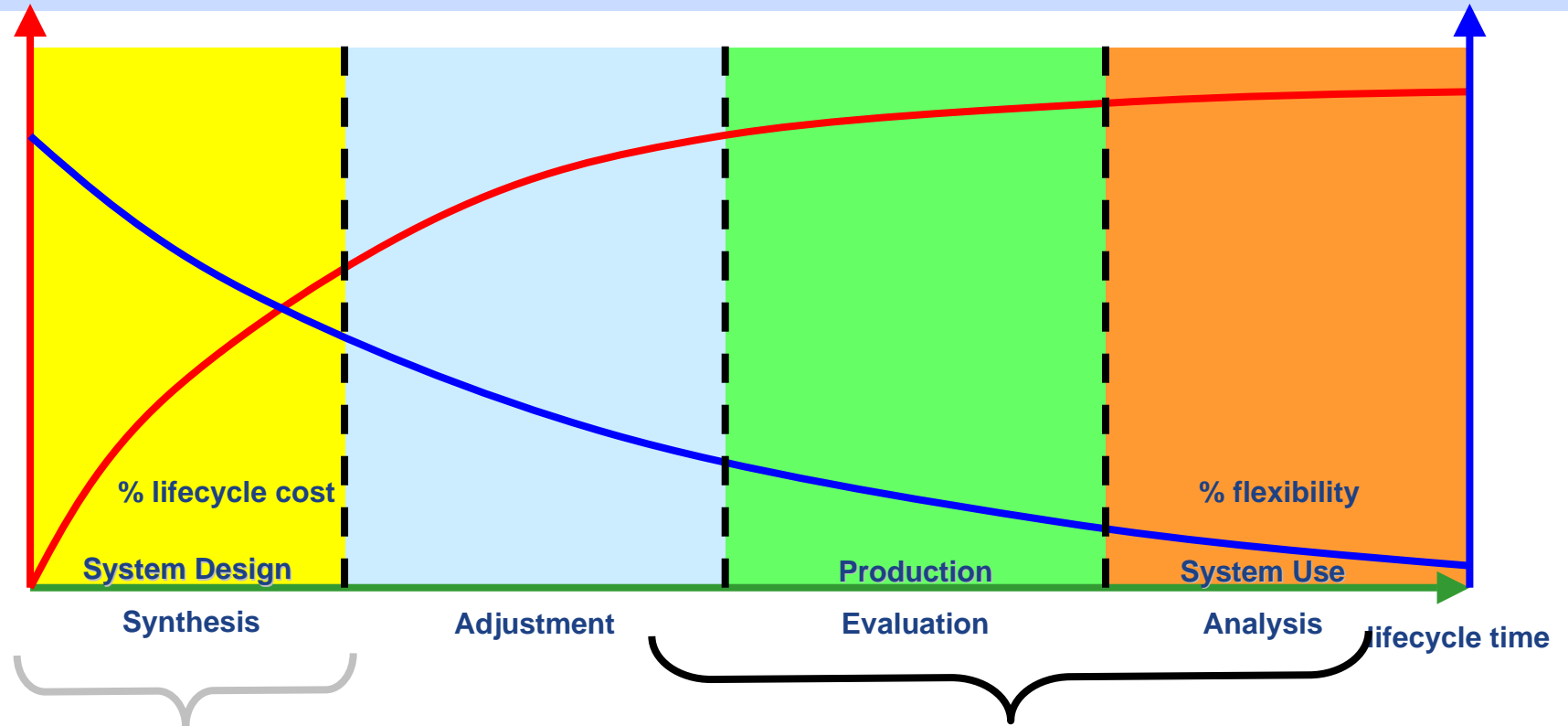
Stages in the iterative design process are separated by key **Decision Points**

Decisions are based on information subject to:

- *Imprecision*
- *Uncertainty*
- *Negotiation*
- *Misinterpretation*

Use **model-based** design and analysis of **dynamics**, **imprecision**, and **uncertainty** to reduce decision **risk**

How to increase impact of Control Theory on Design?



*Future: design beneficial dynamics
=> exploit flexibility at low cost*

*Today: attempt to fix detrimental
dynamics late in design cycle*

Success Stories ← *Education* ← *Credibility* ← *Problem*

↓
Management

↓
*Technical
personnel*

